

CERTIFICATE

I, Tadashi UEDA, residing at 1994-152, Hazama-machi, Hachioji-shi Tokyo 193-0941 Japan, hereby certify that I am the translator of the attached document, namely a Certified Copy of Japanese Patent Application No. 2002-355348 and certify that the following is a true translation to the best of my knowledge and belief.

Tadashi Ueda
Signature of Translator

June 9, 2006
Date

[Name of Document] SPECIFICATION

[Title of the Invention] WAVELENGTH MULTIPLEXING ON-CHIP
OPTICAL INTERCONNECTION CIRCUIT, ELECTRO-OPTICAL DEVICE,
AND ELECTRONIC APPARATUS

[Claims]

[Claim 1] A wavelength multiplexing on-chip optical
interconnection circuit, comprising:

a plurality of circuit blocks provided on one
integrated circuit chip; and

an optical waveguide provided on the integrated circuit
chip as a transmission line for transmitting a plurality of
light components having different wavelengths between the
circuit blocks.

[Claim 2] A wavelength multiplexing on-chip optical
interconnection circuit according to Claim 1,

wherein the circuit blocks are optically and
electrically connected to each other.

[Claim 3] A wavelength multiplexing on-chip optical
interconnection circuit according to Claim 1 or 2,

wherein at least a part of the optical waveguide is
provided on top surfaces of the circuit blocks.

[Claim 4] A wavelength multiplexing on-chip optical
interconnection circuit according to any one of Claims 1 to
3,

wherein at least a part of the optical waveguide is

provided on the circuit blocks to traverse the circuit blocks.

[Claim 5] A wavelength multiplexing on-chip optical interconnection circuit according to any one of Claims 1 to 4,

wherein at least a part of the optical waveguide is provided to detour around the circuit blocks.

[Claim 6] A wavelength multiplexing on-chip optical interconnection circuit according to any one of Claims 1 to 5,

wherein a light emitting element or a light receiving element is electrically connected to each of the circuit blocks,

wherein the light emitting element emits a light component having a predetermined wavelength into the optical waveguide, and

wherein the light receiving element receives a light component having a predetermined wavelength from the optical waveguide.

[Claim 7] A wavelength multiplexing on-chip optical interconnection circuit according to Claim 6,

wherein the light emitting element is a first micro-tile shaped element having a micro-tile shape,

wherein a plurality of the first micro-tile shaped elements is provided on the integrated circuit chip,

wherein each of the first micro-tile shaped elements emits any one of the two or more light components having different wavelengths,

wherein the light receiving element is a second micro-tile shaped element having a micro-tile shape,

wherein a plurality of the second micro-tile shaped elements is provided on the integrated circuit chip, and

wherein each of the second micro-tile shaped elements selectively receives any one of the two or more light components having different wavelengths.

[Claim 8] A wavelength multiplexing on-chip optical interconnection circuit according to Claim 7,

wherein at least a part of the optical waveguide is covered with the first micro-tile shaped elements or the second micro-tile shaped elements.

[Claim 9] A wavelength multiplexing on-chip optical interconnection circuit according to any one of Claims 1 to 8,

wherein the circuit blocks are any one of a CPU, a memory circuit, a DSP, an RF amplifying circuit, an image sensor, and a bio sensor, and

wherein the optical waveguide is a transmission line of data signals or clock signals.

[Claim 10] A wavelength multiplexing on-chip optical interconnection circuit according to any one of Claims 7 to

9,

wherein a plurality of the first micro-tile shaped elements or a plurality of the second micro-tile shaped elements is provided on one of the circuit blocks, and

wherein the plurality of first micro-tile shaped elements or the plurality of second micro-tile shaped elements have emitting wavelengths or receiving wavelengths different from each other.

[Claim 11] A wavelength multiplexing on-chip optical interconnection circuit according to any one of Claims 1 to 10,

wherein a plurality of the integrated circuit chips is mounted on a substrate, and

wherein the plurality of integrated circuit chips are optically connected to each other at least through the micro-tile shaped elements having a light emitting function or a light receiving function and the optical waveguide provided on the substrate.

[Claim 12] A wavelength multiplexing on-chip optical interconnection circuit according to any one of Claims 1 to 11,

wherein the integrated circuit chips is mounted on a substrate,

wherein the integrated circuit chips are tightly bonded to each other, and

wherein the integrated circuit chips are optically or electrically connected to each other.

[Claim 13] An electro-optical device comprising the wavelength multiplexing on-chip optical interconnection circuit according to any one of Claims 1 to 12.

[Claim 14] An electronic apparatus comprising the wavelength multiplexing on-chip optical interconnection circuit according to any one of Claims 1 to 12.

[Detailed Description of the Invention]

[0001]

[Technical Field of the Invention]

The present invention relates to a wavelength multiplexing on-chip interconnection circuit, an electro-optical device, and an electronic apparatus.

[0002]

[Description of the Related Art]

Originally, LSIs (Large Scale Integrated circuits) have been progressed as integrated circuits having specific functions such as DRAM (Dynamic Random Access Memory) or MPU (Micro Processing Unit), but with the speedup of MPUs, SRAMs (Static Random Access Memories), used as high-speed cache memories, have been integrated on the same chip. Now, by integrating flash memory, DSP (Digital Signal Processor), DRAM, etc. on one chip, the LSIs are being developed into high-performance information processing systems.

[0003]

The LSI obtained by forming the integrated circuits having different functions on the same chip as described above is referred to as SOC (System On a Chip). In the SOC, the integrated circuits (circuit blocks) having different functions are two-dimensionally formed on one chip, and the respective circuit blocks are connected to each other through electrical wiring referred to as global wiring. An operating speed of the SOC is restricted by problems such as the signal delay or the increase in power consumption of the global wiring (see, for example, Non-patent Document 1).

[0004]

[Non-Patent Document 1]

Applied Physics, Vol. 71, No. 9 (2002), p1091-1101,
published by Japanese Applied Physics Society

[0005]

If signal transmission between the respective circuit blocks can be performed using optical signals, the signal delay or the increase in power consumption generated in the electrical global wiring can be avoided, thereby remarkably enhancing the processing speed of LSI.

Then, in order to transmit data using the optical signals, optical transmission means for transmitting the optical signals emitted from a light source to a predetermined place and inputting the optical signals to

light receiving elements, etc. is required. Conventionally, as such optical transmission means, an art using optical fibers or an art using optical waveguides formed on a substrate were used.

[0006]

[Problems to be Solved by the Invention]

However, when the optical fiber is used as optical transmission means, connection to optical components such as a light emitting element and a light receiving element is complicated, so that there are problems that much cost and time are required for manufacture thereof and miniaturization of the optical transmission means is difficult.

[0007]

On the other hand, it can be considered that the connection of an optical transmission medium to the light emitting element and the light receiving element is simplified using an optical waveguide formed on a substrate. However, input/output structures suitable for the optical waveguide are not found out yet, and the optical transmission means with fineness and facilitation of manufacture enough to apply to the integrated circuits is not developed yet.

[0008]

The present invention has been made in view of the

above situations. It is an object of the present invention to provide a wavelength multiplexing on-chip optical interconnection circuit, an electro-optical device, and an electronic apparatus, wherein a signal transmission speed can be enhanced, fineness can be easily accomplished, and they can be simply manufactured.

[0009]

[Means for Solving the Problems]

In order to accomplish the above object, a wavelength multiplexing on-chip optical interconnection circuit according to the present invention comprises a plurality of circuit blocks provided on one integrated circuit chip, and an optical waveguide provided on the integrated circuit chip as a transmission line for transmitting a plurality of light components having different wavelengths between the circuit blocks.

According to the present invention, data can be transmitted at a very high speed between the plurality of circuit blocks provided on one integrated circuit chip (an IC chip or an LSI chip) using optical signals (optical pulse signals, etc.) to be propagated through the optical waveguide. Further, according to the present invention, since the optical signals having different wavelengths are transmitted through one optical waveguide, data can be transmitted at a higher speed with a compact structure.

Therefore, according to the present invention, by constructing, for example, CPU and a memory unit, etc as the circuit blocks, it is possible to improve the signal transmission speed between CPU and the memory unit, which was a bottleneck in the conventional computer system by leaps and bounds.

[0010]

In the wavelength multiplexing on-chip optical interconnection circuit according to the present invention, it is preferable that the circuit blocks are optically and electrically connected to each other.

According to the present invention, the circuit blocks can be optically connected to each other using the optical waveguide, and can be electrically connected to each other using metal wiring lines. As a result, signals and power supply not requiring a relative high-speed transmission can be electrically transmitted through the metal wiring lines, and signals requiring a high-speed transmission can be transmitted through the optical waveguide. Therefore, according to the present invention, it is possible to provide a system having a simple and compact structure and capable of processing signals at a high speed as a whole.

[0011]

Further, in the wavelength multiplexing on-chip optical interconnection circuit according to the present invention,

it is preferable that at least a part of the optical waveguide is provided on top surfaces of the circuit blocks.

According to the present invention, for example, a light emitting element or a light receiving element provided on the top surfaces of the circuit blocks can be optically connected to the optical waveguide. Therefore, according to the present invention, it is possible to enhance the degree of freedom in arrangement of the optical waveguide, the light emitting element and the light receiving element, so that it is possible to provide the wavelength multiplexing on-chip optical interconnection circuit having a simple and easily-manufactured structure.

[0012]

Furthermore, in the wavelength multiplexing on-chip optical interconnection circuit according to the present invention, it is also preferable that at least a part of the optical waveguide is provided on the circuit blocks to traverse the circuit blocks.

According to the present invention, since the optical waveguide can be arranged in any region as well as the top surfaces of the circuit blocks, on the integrated circuit chip, it is possible to reduce the length of the optical waveguide.

[0013]

Furthermore, in the wavelength multiplexing on-chip

optical interconnection circuit according to the present invention, it is also preferable that at least a part of the optical waveguide is provided to detour around the circuit blocks.

According to the present invention, for example, when a relatively large step difference is formed at the boundary between the circuit block regions and the non-circuit block region on the integrated circuit chip, it is possible to enhance optical coupling efficiency in the optical waveguide, by providing the optical waveguide to detour around the circuit block regions.

[0014]

Furthermore, in the wavelength multiplexing on-chip optical interconnection circuit according to the present invention, it is preferable that a light emitting element or a light receiving element is electrically connected to each of the circuit blocks, the light emitting element emits a light component having a predetermined wavelength into the optical waveguide, and the light receiving element receives a light component having a predetermined wavelength from the optical waveguide.

According to the present invention, output signals of the circuit block can be converted into optical signals having a predetermined wavelength by means of the light emitting element, and can be propagated through the optical

waveguide. The optical signals having a predetermined wavelength propagated through the optical waveguide can be converted into electrical signals by means of the light receiving element, and can be input to another circuit block. Therefore, according to the present invention, a plurality of light emitting elements or light receiving elements having different emitting wavelengths or different receiving wavelengths can be connected to the circuit blocks, so that the wavelength multiplexing transmission can be executed between the circuit blocks.

[0015]

Furthermore, in the wavelength multiplexing on-chip optical interconnection circuit according to the present invention, it is also preferable that the light emitting element is a first micro-tile shaped element having a micro-tile shape, a plurality of the first micro-tile shaped elements is provided on the integrated circuit chip, each of the first micro-tile shaped elements emits any one of the two or more light components having different wavelengths, the light receiving element is a second micro-tile shaped element having a micro-tile shape, a plurality of the second micro-tile shaped elements is provided on the integrated circuit chip, and each of the second micro-tile shaped elements selectively receives any one of the two or more light components having different wavelengths.

According to the present invention, the first micro-tile shaped element constituting the light emitting element and the second micro-tile shaped element constituting the light receiving element can be formed in a very small shape (for example, having an area of several hundred μm square or less and a thickness of several ten μm or less). In addition, the first micro-tile shaped elements and the second micro-tile shaped elements can be attached to desired positions on the integrated circuit chip or the circuit blocks using an adhesive. Therefore, according to the present invention, it is possible to provide a system having a simple and compact structure, and capable of manufacturing easily and processing signals at a higher speed than that in the conventional art.

[0016]

Furthermore, in the wavelength multiplexing on-chip optical interconnection circuit according to the present invention, it is also preferable that the first micro-tile shaped elements or the second micro-tile shaped elements are attached to the top surfaces of the circuit blocks, and, the first micro-tile shaped elements or the second micro-tile shaped elements and the circuit block are electrically connected to the circuit blocks.

Accordingly, since the first or second micro-tile shaped elements are directly attached to desired positions

on the top surfaces of the circuit blocks, the length of wiring lines between input/output terminal for electrical signals in the circuit blocks and the first or second micro-tile elements can be reduced. Therefore, according to the present invention, it is also possible to accomplish the speedup of transmission and compactness.

[0017]

Furthermore, in the wavelength multiplexing on-chip optical interconnection circuit according to the present invention, it is also preferable that at least a part of the optical waveguide is covered with the first micro-tile shaped elements or the second micro-tile shaped elements.

According to the present invention, all the light components emitted from the first micro-tile shaped elements can be input into the optical waveguide. In addition, the second micro-tile shaped elements can efficiently receive the optical signals propagated through the optical waveguide. Therefore, according to the present invention, it is possible to provide the wavelength multiplexing on-chip optical interconnection circuit having a simple and easily-manufactured structure.

[0018]

Furthermore, in the wavelength multiplexing on-chip optical interconnection circuit according to the present invention, it is also preferable that the circuit blocks are

any one of a CPU, a memory circuit, a DSP, an RF amplifying circuit, an image sensor, and a bio sensor, and the optical waveguide is a transmission line of data signals or clock signals.

According to the present invention, it is possible to improve the signal transmission speed between the CPU and the memory circuit, which was a bottleneck of a high-speed information processing in the conventional computer system, by leaps and bounds. Further, according to the present invention, since the conventional structure in which a bus between the CPU and the memory circuit is constructed using a plurality of metal wiring lines can be replaced with, for example, one optical waveguide and a plurality of micro-tile shaped elements, it is possible to provide a more compact and higher-performance computer system.

[0019]

Furthermore, in the wavelength multiplexing on-chip optical interconnection circuit according to the present invention, it is also preferable that a plurality of the first micro-tile shaped elements or a plurality of the second micro-tile shaped elements is provided on one of the circuit blocks, and the plurality of first micro-tile shaped elements or the plurality of second micro-tile shaped elements have emitting wavelengths or receiving wavelengths different from each other.

According to the present invention, when the plurality of micro-tile shaped elements provided on any circuit block emit optical signals having wavelengths of λ_1 , λ_2 , λ_3 , ..., a plurality of output signals (electrical signals) of the circuit block can be transmitted through one optical waveguide as plural kinds of optical signals. Further, a plurality of second micro-tile shaped elements provided on another circuit block may selectively receive, for example, the optical signals having wavelengths of λ_1 , λ_2 , λ_3 , Therefore, according to the present invention, the wavelength multiplexing transmission can be executed between the circuit blocks or between a circuit block and another circuit, etc.

[0020]

Furthermore, in the wavelength multiplexing on-chip optical interconnection circuit according to the present invention, it is also preferable that a plurality of the integrated circuit chips is mounted on a substrate, and the plurality of integrated circuit chips are optically connected to each other at least through the micro-tile shaped elements having a light emitting function or a light receiving function and the optical waveguide provided on the substrate.

According to the present invention, by providing a plurality of integrated circuit chips having the wavelength

multiplexing on-chip interconnection circuit on a substrate, it is also possible to execute the wavelength multiplexing transmission between the integrated circuit chips.

Therefore, according to the present invention, it is possible to compactly construct a large-scaled computer system or an electro-optical device such as a liquid crystal display device comprising a plurality of integrated circuit chips, and to operate it at a high speed.

[0021]

Furthermore, in the wavelength multiplexing on-chip optical interconnection circuit according to the present invention, it is also preferable that a plurality of the integrated circuit chips is mounted on a substrate, the integrated circuit chips are tightly bonded to each other, and the integrated circuit chips are optically or electrically connected to each other.

According to the present invention, since the integrated circuit chips capable of executing the wavelength multiplexing transmission in a chip are tightly bonded to each other and mounted on a substrate, it is possible to optically and electrically connect the integrated circuit chips each other compactly. Therefore, according to the present invention, it is possible to execute the wavelength multiplexing transmission between the integrated circuit chips as well as within each of the integrated circuit chips,

so that it is possible to further accomplish high-speed operation and compactness of the system comprising a plurality of integrated circuit chips.

[0022]

An electro-optical device according to the present invention comprises the wavelength multiplexing on-chip optical interconnection circuit.

According to the present invention, in an electro-optical device such as a liquid crystal display device, an electroluminescence panel and a plasma display panel, a timing control circuit for generating scanning signals and data signals from image signals and driver circuits, etc. can be constructed using the wavelength multiplexing on-chip optical interconnection circuit. Further, the timing control circuit and the driver circuits, etc. may be constructed as the circuit blocks, so that the timing control circuit and various driver circuits may be constructed using one integrated circuit chip. Therefore, according to the present invention, it is possible to realize compactness of the electro-optical device, enlargement of a screen, and high-quality display.

[0023]

An electronic apparatus according to the present invention comprises the wavelength multiplexing on-chip optical interconnection circuit.

According to the present invention, by constructing an electronic apparatus in which processing units such as a CPU and memory circuits are implemented as a module comprising the wavelength multiplexing on-chip optical interconnection circuit, it is possible to inexpensively provide a compact and high-performance electronic apparatus capable of processing signals at a higher speed than that in the conventional case.

[0024]

[Description of the Embodiments]

Now, a wavelength multiplexing on-chip optical interconnection circuit according to embodiments of the present invention will be described with reference to the accompanying drawings.

(First embodiment)

In this embodiment, a plurality of circuit blocks provided on one integrated circuit chip (an IC chip, an LSI chip, etc.) are optically connected to each other through an optical waveguide. Then, by allowing light components having a plurality of wavelengths to pass through one optical waveguide, independent parallel transmission using optical signals is executed. Fig. 1 is a perspective view illustrating a wavelength multiplexing on-chip optical interconnection circuit according to the first embodiment of the present invention.

[0025]

On one integrated circuit chip 10d, three circuit blocks (sometimes referred to as cores) 240a, 240b, and 240c are formed. The integrated circuit chip 10d is comprised of a semiconductor chip. The number of circuit blocks to be formed on the integrated circuit chip 10d is not limited to three, but may be two or more. Further, on the integrated circuit chip 10d, circuits or electronic elements (for example, constituting pixels), etc. may be formed in addition to the circuit blocks.

[0026]

The circuit blocks 240a, 240b, and 240c constitute a CPU, a memory circuit, an image signal processing circuit, an image signal drive circuit, a communication I/O, various interface circuits, an A/D converter, a D/A converter, etc. For example, the circuit block 240a constitutes a CPU, the circuit block 240b constitutes a first memory circuit, and the circuit block 240c constitutes a second memory circuit. Further, the circuit blocks 240a, 240b, and 240c can be formed on the integrated circuit chip 10d, as a bipolar integrated circuit, a MOS integrated circuit, a CMOS integrated circuit or an SOS (Silicon On Sapphire) integrated circuit, etc.

The circuit blocks 240a, 240b, and 240c are electrically connected to each other through a plurality of

metal wiring lines 231.

[0027]

First micro-tile shaped elements 21 and 21' comprising light emitting elements are attached to a top surface of the circuit block 240a. The first micro-tile shaped elements 22 and 22' are electrically connected to the circuit block 240a. Further, second micro-tile shaped elements 22 and 22' comprising light receiving elements are attached to a top surface of the circuit block 240b. The second micro-tile shaped elements 22 and 22' are electrically connected to the circuit block 240b. Furthermore, the second micro-tile shaped elements 22 and 22' comprising light receiving elements are attached to a top surface of the circuit block 240c. The second micro-tile shaped elements 22 and 22' are electrically connected to the circuit block 240c.

[0028]

The first and second micro-tile shaped elements 21, 21', 22 and 22' are elements having a micro-tile shape. The first micro-tile shaped elements 21 and 21' having a light emitting function comprise, for example, a VCSEL (Vertical Cavity Surface Emitting laser), a built-in DFB (Distributed FeedBack) laser for absorbing and modulating electric field, or LED, etc. The second micro-tile shaped elements 22 and 22' having a light receiving function comprise, for example, a photodiode or a phototransistor. The first and second

micro-tile shaped elements 21, 21', 22 and 22' have an area of several hundred μm square or less and a thickness of several ten μm or less, and are attached to a surface of the integrated circuit chip 10d using an adhesive, etc. A manufacturing method and a mounting method of the first and second micro-tile shaped elements 21, 21', 22 and 22' will be described in detail later.

[0029]

Here, the first micro-tile shaped element 21 selectively emits a light component having a wavelength of λ_1 , and the first micro-tile shaped element 21' selectively emits a light component having a wavelength of λ_2 . On the other hand, the second micro-tile shaped element 22 selectively receives the light component having a wavelength of λ_1 , and the second micro-tile shaped element 22' selectively receives the light component having a wavelength of λ_2 .

[0030]

On the integrated circuit chip 10d, an optical waveguide 30 is also formed. The optical waveguide 30 is made of an optical waveguide material formed in a rod shape on the top surface of the integrated circuit chip 10d, the top surfaces of the circuit blocks 240a, 240b, and 240c, and a top surface of the metal wiring lines 231. It is preferable that a thickness (height) of the optical

waveguide has a value larger than a step difference formed by a surface of the integrated circuit chip 10d and the circuit blocks 240a, 240b, and 240c or by the micro-tile shaped elements and the metal wiring lines 231. Therefore, an optical coupling efficiency in the optical waveguide 30 can be enhanced.

The optical waveguide 30 is not limited to a straight-line shape shown in Fig. 1, but may be provided with a bending or a branch, or may be formed in a loop shape.

[0031]

The optical waveguide material can employ a transparent resin or a sol-gel glass, etc. The optical waveguide material constituting the optical waveguide 30 covers the respective micro-tile shaped elements. Therefore, the first and second micro-tile shaped elements 21, 21', 22 and 22' are optically connected through the optical waveguide 30. That is, the light components (having wavelengths of λ_1 and λ_2 , respectively) emitted from the micro-tile shaped elements 21 and 21' are all input into the optical waveguide 30, and propagated through the optical waveguide 30. Further, the second micro-tile shaped element 22 selectively detects the light component of a wavelength λ_1 from the light components propagated through the optical waveguide 30, and the second micro-tile shaped element 22' selectively detects the light component of a wavelength λ_2 from the

light components propagated through the optical waveguide 30.

Furthermore, on a surface of the optical waveguide material constituting the optical waveguide 30, a light absorbing film or a light reflecting film for preventing a disturbing light from being input may be formed.

[0032]

Because of this construction, for example, a first signal (data) output from the circuit block 240a constituting a CPU is converted into an optical pulse signal having a wavelength of λ_1 by means of the first micro-tile shaped element 21 on the circuit block 240a. Further, a second signal (data) output from the circuit block 240a is converted into an optical pulse signal having a wavelength of λ_2 by means of the first micro-tile shaped element 21' on the circuit block 240a.

[0033]

The optical pulse signals having wavelengths of λ_1 and λ_2 are input to the optical waveguide 30, and propagated through the optical waveguide 30. Then, the optical pulse signal having a wavelength of λ_1 is converted into an electrical signal by means of the second micro-tile shaped element 22 on each of the circuit blocks 240b and 240c, and input as the first signal to the circuit blocks 240b and 240c, respectively. Further, the optical pulse signal having a wavelength of λ_2 is converted into an electrical

signal by means of the second micro-tile shaped element 22' on each of the circuit blocks 240b and 240c, and input as the second signal to the circuit blocks 240b and 240c, respectively.

[0034]

As a result, according to this embodiment, using the first and second micro-tile shaped elements 21, 21', 22 and 22' having a light emitting function or a light receiving function and having a wavelength selectivity, a plurality of signals (first signal, second signal) can be independently and simultaneously transmitted in parallel between the circuit blocks 240a, 240b, and 240c on the integrated circuit chip 10d. That is, according to this embodiment, since a wavelength multiplexing transmission can be executed between the circuit blocks 240a, 240b, and 240c on the integrated circuit chip 10d, it is possible to transmit data at a very high speed.

[0035]

The signal to be propagated through the optical waveguide 30 is not limited to data, but may be a clock signal. For example, a first clock signal is emitted from the first micro-tile shaped element 21 of the circuit block 240a, and a second clock signal is emitted from the second micro-tile shaped element 21' of the circuit block 240b. The first clock signal is propagated through the optical

waveguide 30, detected by the second micro-tile shaped elements 22 of the other circuit blocks 240b and 240c, and then input to the circuit blocks 240b and 240c. Further, the second clock signal is propagated through the optical waveguide 30, detected by the second micro-tile shaped elements 22' of the other circuit blocks 240b and 240c, and then input to the circuit blocks 240b and 240c. As a result, since a plurality of kinds of clock signals having relatively high frequencies can be distributed using one optical waveguide 30, a plurality of circuit blocks 240a, 240b, and 240c can be operated at a high speed.

[0036]

Furthermore, in this embodiment, since the electrical signals are converted into optical signals by means of the first and second micro-tile shaped elements 21, 21', 22 and 22' attached onto the respective circuit blocks 240a, 240b, and 240c, it is possible to simply manufacture a very compact optical-signal transmission means.

[0037]

Furthermore, in this embodiment, the circuit blocks 240a, 240b, and 240c are electrically connected to each other through metal wiring lines 231. Therefore, signals and power supply not requiring transmission of a relatively high speed can be transmitted through the metal wiring lines 231.

[0038]

Furthermore, in this embodiment, the optical waveguide 30 traverses the circuit block 240b. Therefore, the length of the optical waveguide 30 can be shortened. The optical waveguide 30 can be formed in any region as well as the top surfaces of the circuit blocks 240a, 240b, and 240c, on the integrated circuit chip 10d.

[0039]

The optical waveguide 30 may be formed on a surface of the integrated circuit chip 10d so as to detour around the circuit blocks 240a, 240b, and 240c. As a result, even when the surfaces of the circuit blocks 240a, 240b, and 240c and the other surface of the other region on the surface of the integrated circuit chip 10d form a large step difference, since the optical waveguide 30 can be provided on a flat surface, it is possible to enhance the optical coupling efficiency in the course of the transmission of optical signals.

[0040]

In the embodiment shown in Fig. 1, although the first micro-tile shaped elements 21 and 21' having a light emitting function are provided on the circuit block 240a and the second micro-tile shaped elements 22 and 22' having a light receiving function are provided on the circuit blocks 240b and 240c, each of the circuit blocks 240a, 240b, and

240c may be provided with the first micro-tile shaped elements 21 and 21' and the second micro-tile shaped elements 22 and 22'. As a result, the bidirectional wavelength multiplexing transmission can be executed between the circuit blocks 240a, 240b, and 240c.

[0041]

In the embodiment shown in Fig. 1, although the circuit blocks 240a, 240b, and 240c are connected each other through one optical waveguide 30, the circuit blocks 240a, 240b, and 240c may be connected each other using a plurality of optical waveguides 30 and a plurality of first and second micro-tile shaped elements 21, 21', 22 and 22' connected to the optical waveguides. Further, in the embodiment shown in Fig. 1, although all the circuit blocks 240a, 240b, and 240c are connected each other through the optical waveguide 30, only some of the circuit blocks (for example, the circuit block 240a and the circuit block 240b) may be connected each other through the optical waveguide 30.

[0042]

The integrated circuit chip 10d shown in Fig. 1 can be mounted on a desired substrate, where it is preferable that it is mounted using a flip-chip mounting method. As a result, it is possible to simply and compactly construct a high-performance information processing system. Furthermore, a plurality of integrated circuit chips 10d may be mounted

on a desired substrate. In this case, it is preferable that side surfaces of the integrated circuit chips 10d are tightly bonded to each other and then arranged on the substrate. The integrated circuit chips 10d can be tightly bonded to each other and mounted simply using the flip-chip mounting method.

[0043]

Through such mounting method, surfaces or rear faces of the integrated circuit chips 10d can be attached to one plane to form a sheet of plane, and then the optical waveguide 30 can be easily formed on the sheet of plane. Accordingly, it is also possible to easily connect the integrated circuit chips 10d to each other through the optical wave guide 30 and the first and second micro-tile shaped elements 21, 21', 22 and 22'. Therefore, it is possible to simply enhance compactness and performance of a large-scaled computer system comprising a plurality of integrated circuit chips 10d.

[0044]

In this embodiment, for example, the circuit block 240a constitutes CPU, the circuit block 240b constitutes a first memory circuit, and the circuit block 240c constitutes a second memory circuit. Then, data can be transmitted concurrently from the CPU to a plurality of memory circuits. As a result, a striping of dividing and concurrently writing

one or plural data output from the CPU in the plurality of memory circuits can be performed, and thus it is possible to further speed up the data transmission between the CPU and the memory circuits.

[0045]

(Optical interconnection circuit)

Next, an optical interconnection circuit, which is an element of the wavelength multiplexing on-chip optical interconnection circuit according to this embodiment, will be described in detail. Hereinafter, although a case will be described that the optical interconnection circuit comprising micro-tile shaped elements and an optical waveguide is formed on a surface of a substrate 10, this optical interconnection circuit may be formed on the integrated circuit chip 10d shown in Fig. 1, similarly.

[0046]

Fig. 2 shows the optical interconnection circuit according to this embodiment, where (a) is a schematic side view and (b) is a schematic plan view. The same elements as those in the above embodiment are indicated by the same reference numerals.

The optical interconnection circuit according to this embodiment comprises a first micro-tile shaped element 21 and a second micro-tile shaped element 22 attached on a substrate 10, and an optical waveguide 30 comprising an

optical waveguide material formed on a surface of the substrate 10 to connect the first micro-tile shaped element 21 and the second micro-tile shaped element 22 to each other. As the optical waveguide material constituting the optical waveguide 30, a transparent resin or a sol-gel glass can be employed. As the substrate 10, any one of glass epoxy, ceramics, plastic, polyimide, silicon or glass can be employed.

[0047]

The first micro-tile shaped element 21 includes a light emitting part 21a capable of emitting light. The second micro-tile shaped element 22 includes a light receiving part 22b capable of receiving light. Accordingly, at least the light emitting part 21a of the first micro-tile shaped element 21 and the light receiving part 22b of the second micro-tile shaped element 22 are covered with the optical waveguide material, which constitutes the optical waveguide 30.

[0048]

With such configuration, the light emitted from the light emitting part 21a of the first micro-tile shaped element 21 propagates along the optical waveguide 30 and reaches the light receiving part 22b of the second micro-tile shaped element 22. Accordingly, when an optical signal is emitted from the light emitting part 21a by controlling

the light emitting operation of the light emitting part 21a, the optical signal propagates along the optical waveguide 30, and the optical signal can be detected from the light receiving part 22b.

[0049]

Further, the optical signal emitted from the first micro-tile shaped element 21 propagates along the optical waveguide 30 and is incident on the second micro-tile shaped element 22. Furthermore, the signal passes through the second micro-tile shaped element 22. As a result, it is possible to almost simultaneously transmit optical signals from one micro-tile shaped element 21 to a plurality of second micro-tile shaped elements 22. Herein, when a thickness of the second micro-tile shaped element 22 is set to 20 μm or less, the step difference between the substrate and the second micro-shaped element 22 becomes too small. Thus, as shown in Fig. 2, the optical waveguide 30 can be formed consecutively regardless of the step difference. When the optical waveguide 30 is formed consecutively at the step portion, light transmission loss such as scattering can be neglected because the step difference is too small. For such reason, a specific configuration or an optical element for alleviating the step difference is unnecessary. Thus, it is possible to simply and cheaply fabricate it. Further, the thickness of the optical waveguide material forming the

optical waveguide 30 can be dozen or less micrometers.

[0050]

The first micro-tile shaped element 21 includes, for example, an LED, a VCSEL, and a DFB laser having an electric field absorbing modulator. Although the LED has the simplest structure and the easiest fabrication step as a light emitting device, the modulation speed of the optical signals is about several hundreds Mbps slow. In contrast, because the VCSEL has a high-speed modulation of 10 Gbps or more, a small threshold current, and high light-emitting efficiency, it is possible to drive it with low power consumption. In the DFB laser, although modulation speed is 1 Gbps, less than that of the VCSEL, it is possible to transmit optical signals with higher efficiency than the VCSEL since laser beam is emitted from the end of the micro-tile shape in a direction parallel to the plane of the substrate 10, i.e., in a direction along the optical waveguide 30.

[0051]

The second micro-tile shaped element 22 includes, for example, a photodiode or a phototransistor. Herein, as the photodiode, a PIN type photodiode, an APD (Avalanche Photo Diode), an MSM type photo diode can be selected for use. The APD has high photosensitivity and response frequency. The MSM type photodiode has a simple structure and is easily

integrated with an amplifying transistor.

[0052]

Further, a third micro-tile shaped element (not shown), which is composed of a light receiving element, may be formed to overlap the first micro-tile shaped element 21. When being formed in such manner, the quantity of light emitted from the first micro-tile shaped element 21 is monitored by the third micro-tile shaped element, and then the monitored value is fed back to the first micro-tile shaped element 21. As a result, APC function is attained, so stable optical data transmission can be achieved. Alternatively, the APC function may be built in the first micro-tile shaped element 21 itself. Further, it is desirable that the second micro-tile shaped element 22 should have an circuit for amplifying the detected signals. As a result, it is possible to further improve the efficiency of the apparatus.

[0053]

Hence, the first micro-tile shaped element 21 and the second micro-tile shaped element 22 are electrically connected to an electronic circuit (not shown), such as an integrated circuit, an EL display circuit, a plasma display, a liquid crystal display circuit, and the like, which are provided on the substrate 10. As a result, the computer system including integrated circuits can be compact and have

higher operation speed than a conventional art. Further, by means of the optical interconnection circuit according to the present embodiment, it is possible to transmit the scanning signals of a planar display provided on the substrate 10 at high speed and to achieve the planar display device having the enlarged screen and high quality images.

[0054]

In Fig. 2, although each of the first micro-tile shaped element 21 and the second micro-tile shaped element 22 is coupled to one optical waveguide 30, the number of the second micro-tile shaped element 22 may be a plural number. In such case, it is possible for optical signals emitted from one first micro-tile shaped element 21 (light emitting element) to propagate along the optical waveguide 30 and to be simultaneously detected at the plurality of second micro-tile shaped elements 22. This is equal to the bus lines having a set more.

[0055]

In addition, the first micro-tile shaped elements 21 and the second micro-tile shaped elements 22 may be a plural number. In such case, it is preferable that the wavelength of light emitted from each of the first micro-tile shaped elements 21 be different. And, it is desirable that each of the second micro-tile shaped elements 22 should be light receiving means capable of selecting a wavelength

corresponding to wavelength of the light emitted from at least one first micro-tile shaped element 21. As a result, it is possible for optical signals emitted from the first micro-tile shaped elements 21 to propagate one optical waveguide 30 simultaneously and to be detected at the respective second micro-tile shaped elements 22. Thus, it is possible to simply and easily constitute a bus capable of transmitting and receiving optical signals in parallel.

[0056]

In addition, while being formed in a liner shape shown in Fig. 2, the optical waveguide 30 may be formed in a curved shape or in a branch shape. Further, the optical waveguide 30 may be formed in a loop shape. It is also preferable that the optical waveguide 30 should be formed in a sheet shape to cover the plurality of tile-shaped elements. To be sure, it is also possible for plural sets of the first micro-tile shaped element 21, the second micro-tile shaped element 22, and the optical waveguide 30 to be formed on the top surface of one substrate 10. Moreover, it is also possible for the first micro-tile shaped element 21, the second micro-tile shaped element 22, and the optical waveguide 30 to be formed on both of the front and back surfaces of the substrate 10.

[0057]

Then, a modified example of the optical interconnection

circuit according to the present embodiment will be described with reference to Figs. 3 to 6. Unlike the embodiment of Fig. 2, the present embodiment has a light scattering frame for scattering light at the optical waveguide 30 positioned in the neighborhood of the first micro-tile shaped element 21 and the second micro-tile shaped element 22. Fig. 3 is a schematic side view illustrating a modified example of the optical interconnection circuit according to the present embodiment.

[0058]

In the optical interconnection circuit, light scattering particles forming a light scattering frame 31a are dispersed in the neighborhood of the first micro-tile shaped element 21 and the second micro-tile shaped element 22, in the optical waveguide material forming the optical waveguide 30. For example, silica particle, glass particle, metal particle, or the like may be used as the light scattering particle. The optical waveguide 30 having such light scattering frame 31a uses a liquid droplet discharging manner for discharging a liquid droplet from, for example, a dispenser, an inkjet nozzle, or the like. Specifically, the optical waveguide 30 having such light scattering frame 31a is formed by discharging a liquid optical waveguide material (such as resin) from one inkjet nozzle to a desired portion and then by discharging a liquid optical waveguide material

containing light scattering particles from another inkjet nozzle to a desired portion.

[0059]

Further, except for resin, sol gel glass can be used as a material forming the optical waveguide 30. A method of fabricating the sol gel glass comprises the steps of: adding an acid to metal alkoxide to form a liquid mixture; hydrolyzing the liquid mixture; applying the hydrolyzed solution to a desired portion; and adding energy such as heat to it so as to make glass.

[0060]

Fig. 4 is a schematic side view illustrating another modified example of the optical interconnection circuit according to the present embodiment. A light scattering frame 31a' of the optical interconnection circuit is a dome-shaped light scattering frame made of resin or glass, in which light scattering particles are dispersed. An optical waveguide 30 is formed to cover such light scattering frame 31a' (the dome-shaped light scattering frame). As compared with the light scattering frame 31a shown in Fig. 3, it is possible to easily adjust the optical coupling efficiency between the optical waveguide 30 and a first micro-tile shaped element 21 or a second micro-tile shaped element 22 because it is easy to control the size and shape of such light scattering frame 31a'.

[0061]

Next, a method of fabricating the light scattering frame 31a' will be described. First, an acid is added to metal alkoxide, such as liquid resin or silica ethyl containing light scattering particles, using an inkjet, a dispenser, and the like, and the liquid mixture is hydrolyzed. Then the hydrolyzed solution is applied to a desired portion of a substrate 10 in a dome shape. Then, energy such as heat is supplied to the applied portion to make the solution hardened or glassed. As a result, the light scattering frame 31a' having a dome shape is formed on the first micro-tile shaped element 21 and the second micro-tile shaped element 22. Next, a linear-shaped optical waveguide 30 is formed using transparent resin or sol gel glass to cover the light scattering frame 31a' having a dome shape.

[0062]

Fig. 5 is a schematic side view illustrating another modified example of the optical interconnection circuit according to the present embodiment. A light scattering frame 31b of the optical interconnection circuit has a configuration in which a surface of an optical waveguide material forming an optical waveguide 30 has concave and convex portions. Such light scattering frame 31b is also formed in the neighborhood of a first micro-tile shaped

element 21 and a second tile-shaped element 22. Herein, the concave and convex portions constituting the light scattering frame 31b are formed by an embossing process or a stamper transfer.

[0063]

Fig. 6 illustrates a modified example of the optical interconnection circuit according to the present embodiment. (a) is a schematic side view thereof, and (b) is a schematic plan view thereof. A light scattering frame 31c of the optical interconnection circuit has a configuration in which the line width and height of a linear-shaped optical waveguide material forming an optical waveguide 30 vary. That is, in the optical waveguide 30, the line width and height of the optical waveguide material is narrow in the neighborhood of a light receiving part 22b of the second micro-tile shaped element 22.

[0064]

Hereinafter, a method of fabricating the optical waveguide 30 having the light scattering frame 31c will be described. First, a first micro-tile shaped element 21 and the second micro-tile shaped 22 adhere to a predetermined portion of the surface of the substrate 10. Then, liquid repellent treatment is performed to the entire surface of the substrate 10 and the entire surfaces of the first micro-tile shaped element 21 and the second micro-tile shaped 22.

Next, on the surface subjected to the liquid repellent treatment, a lyophilic treatment is performed to a portion in which the optical waveguide 30 is formed. Herein, a linear shaped portion subjected to a lyophilic treatment has a pattern of narrowed line width in the neighborhood of the light receiving part 22b of the second micro-tile shaped element 22. Further, the lyophilic treatment is executed by for example, the radiation of UV light.

[0065]

Next, a liquid optical waveguide material is dropped from the inkjet nozzle to the inside of the portion subjected to the lyophilic treatment. Accordingly, such a dropped optical waveguide material is permeated into the portion subjected to the lyophilic treatment and is repelled from the portion subjected to the liquid repellent treatment, and surface tension acts thereto. As a result, such a optical waveguide material becomes a narrowed width portion in the neighborhood of the light receiving part 22b as shown in Fig. 6.

[0066]

As described above, in the optical waveguide 30, by providing the light scattering frames 31a, 31b, 31c in the neighborhood of the first micro-tile shaped element 21, it is possible that the optical signals emitted from the first micro-tile shaped element 21 are scattered from the light

scattering frames 31a, 31b, and 31c and effectively propagate along the whole optical waveguide. Further, by providing the light scattering frames 31a, 31b, and 31c in the neighborhood of the second micro-tile shaped element 22, it is possible that optical signals, which have propagated along the optical waveguide 30, are scattered in the neighborhood of the second micro-tile shaped element 22 and are effectively incident on the second micro-tile shaped element 22.

[0067]

Next, still another modified example of the optical interconnection circuit according to the present embodiment will be described with reference to Figs. 7 to 9. Unlike the embodiment described above, the present embodiment has light reflecting frames for reflecting light at the neighborhood of a first micro-tile shaped element 21 in an optical waveguide 30 and a second micro-tile shaped element 22 or at the end of an optical waveguide 30. Fig. 7 illustrates a modified example of the optical interconnection circuit according to the present embodiment. (a) is a schematic side view thereof, and (b) is a schematic plan view thereof.

[0068]

For example, the light reflecting frames 32a and 32b are formed by forming a metal film over the surface of an

optical waveguide material forming the optical waveguide 30. In addition, the light reflecting frames 32a and 32b may be formed by applying paints containing metal fine particles over the surface of the optical waveguide material forming the optical waveguide 30. Fine particles, such as silver, aluminum, magnesium, copper, nickel, titanium, chrome, and zinc, are applied as the metal fine particles. It is preferable that paints should be discharged from the inkjet nozzle to form the metal film and to apply paints containing the metal fine particles forming the light reflecting frames 32a and 32b. It is also possible that the light reflecting frame 32a and the light reflecting frame 32b may be formed over the whole optical waveguide 30.

[0069]

With such configuration, the optical signals emitted from the first micro-tile shaped element 21 are reflected from the light reflecting frame 32a in a direction along the optical waveguide 30, and a part of the optical signals is reflected from the light reflecting frame 32b to the direction of the second micro-tile shaped element 22. Thus, according to the present embodiment, it is possible to effectively transmit optical signals.

[0070]

Fig. 8 illustrates still another modified example of the optical interconnection circuit according to the present

embodiment. (a) is a schematic side view thereof, and (b) is a schematic plan view thereof. A light reflecting frame 32c of the optical interconnection circuit has a configuration in which a reflecting plate with a reflecting surface is attached to an end of the optical waveguide 30. Herein, the reflecting surface of the reflecting frame 32c is provided to incline, e.g., 45° with respect to the surface of the substrate 10.

[0071]

Further, two optical waveguide 30a and 30b, which are parallel to each other, are provided in the optical interconnection circuit. Moreover, the light reflecting frame 32c is composed of a reflecting plate which is provided at one end of two optical waveguides 30a and 30b and is commonly used for the two optical waveguides 30a and 30b. Thus, the optical signals emitted from each of two first micro-tile shaped elements 21 are reflected by the light reflecting frame 32c in a direction along the optical waveguides 30a and 30b, respectively. As a result, according to the present embodiment, it is possible to effectively transmit optical signals and to effectively fabricate an optical interconnection circuit.

Moreover, in Fig. 8, although the common reflecting frame 32c is provided to two optical waveguides 30a and 30b, the common light reflecting frame 32c may preferably be

provided to three or more optical waveguides.

[0072]

Fig. 9 illustrates still another modified example of the optical interconnection circuit according to the present embodiment. (a) is a schematic side view thereof, and (b) is a schematic plan view thereof. Light reflecting frames 32d and 32e of the present optical interconnection circuit are plate-shaped optical components (grating components) in which grating is performed. On the optical waveguide 30, the light reflecting frame 32d is provided to cover the first micro-tile shaped element 21, and the light reflecting frame 32e is provided to cover the second micro-tile shaped element 22.

[0073]

Herein, when the distance between an optical waveguide 30a and an optical waveguide 30b is relatively large, as shown in Fig. 9, light reflecting frames 32e are individually attached to the optical waveguides 30a and 30b. When the optical waveguide 30a and the optical waveguide 30b are provided to be close and almost parallel to each other, as shown in Fig. 9, a light reflecting frame 32d may be attached in common to the optical waveguide 30a and the optical waveguide 30b.

[0074]

In the light scattering frames and light reflecting

frames illustrated in Figs. 3 to 9, it is effective to use a combination thereof. Further, higher performance wavelength multiplexing on-chip optical interconnection circuit can be realized by combining constitutions from the above described first embodiment to the present embodiment.

[0075]

(Fabricating Method)

Next, a method of fabricating the optical waveguide 30 of the optical interconnection circuit according to the present embodiment will be described with reference to Figs. 10 to 13. Fig. 10 is a schematic side view illustrating a method of fabricating the optical waveguide 30.

[0076]

First, the first micro-tile shaped element and the second micro-tile shaped element are attached to the top surface of the substrate 10. Then, a process for fabricating the optical waveguide 30 is performed. And as shown in Fig. 10(a), photo-curable resin 30c in a liquid state is coated over the entire surface of the substrate 10 and the surfaces of the first micro-tile shaped element and the second micro-tile shaped element (not shown). Such coating may be performed by a spin coating method, a roll coating method, a spray coating method, and the like.

[0077]

Next, UV light radiates to the liquid photo-curable

resin 30c with a desired pattern mask. As a result, only the desired region of the liquid photo-curable resin 30c is hardened to be patterned. Further, as shown in Fig. 10(b), the optical waveguide 30d made of a hardened optical waveguide material is formed by removing, for example, cleaning, a resin not hardened.

[0078]

Fig. 11 is typical side views illustrating another example of the method of fabricating the optical waveguide 30. First, the first micro-tile shaped element and the second micro-tile shaped element are attached to the top surface of the substrate 10. Then, a process for fabricating the optical waveguide 30 is performed. Then, as shown in Fig. 11(a), resin 30e is coated over the top surface of the substrate 10 and the entire top surfaces of the first micro-tile shaped element and the second micro-tile shaped element (not shown). Such coating may be performed by a spin coating method, a roll coating method, a spray coating method, and the like. Then, a resist mask 41 is formed at the desired region on the resin 30e. The region on which the resist mask 41 is formed is equal to a region where the optical waveguide 30 is formed.

[0079]

Next, as shown in Fig. 11(b), dry etching or wet etching is performed to the entire substrate 10 with the

resist mask 41, and resin e except for a portion under the resist mask 41 is removed. The optical waveguide 30f made of an optical waveguide material is formed by removing the resist mask 41 through photolithography patterning.

[0080]

Fig. 12 is typical side views illustrating another example of the method of fabricating the optical waveguide 30. First, the first micro-tile shaped element and the second micro-tile shaped element are attached to the top surface of the substrate 10. Then, the process for fabricating the optical waveguide 30 is performed. Then, the liquid repellent treatment is performed to the surface of the substrate 10 and the entire surfaces of the first micro-tile shaped element and the second micro-tile shaped element (not shown) to provide a liquid repellent surface 51.

[0081]

Next, as shown in Fig. 12(a), UV light radiates to the desired patterned region of the liquid repellent surface 51, and thereby a desired patterned lyophilic surface 52 is provided into the liquid repellent surface 51. Then, as shown in Fig. 12(b), a liquid optical waveguide material 30g is dropped from an inkjet nozzle, a dispenser, and the like to the lyophilic region 52. Transparent resin or sol gel glass may be used as the optical waveguide material 30g. Then, the optical waveguide 30h made of the optical

waveguide material is formed by hardening the optical waveguide material 30g dropped on the substrate 10.

When the optical waveguide material 30g is made of sol gel glass, an acid is added to metal alkoxide, and then hydrolyzed solution and the like is dropped from the inkjet nozzle or the dispenser to the lyophilic surface 52. Then, energy such as heat is applied to the dropped solution to make glass, thereby forming an optical waveguide 30h.

[0082]

Fig. 13 is typical side views illustrating another example of the method of fabricating the optical waveguide 30. First, the first micro-tile shaped element and the second micro-tile shaped element are attached to the top surface of the substrate 10. Then, a process for fabricating the optical waveguide 30 is performed. Hence, as shown in Fig. 13(a), liquid resin 30i is applied over the top surface of the substrate 10 and the top surfaces of the first micro-tile shaped element and the second micro-tile shaped element to cover a region in which the optical waveguide 30 will be formed.

[0083]

Next, a stamper 51 having a pattern shape 52 of the optical waveguide 30 is pressed into a surface of the substrate 10 from the upper part of the substrate 10. Then, as shown in Fig. 13(b), the stamper 51 is raised from the

substrate 10. As a result, by means of a pattern transfer method using the stamper 51, an optical waveguide 30j made of an optical waveguide material is formed in the desired pattern on the substrate 10.

[0084]

Other than the method illustrated in Figs. 10 to 13, it is preferable that a method of fabricating the optical waveguide 30 described below should be used. For example, it is preferable that an optical waveguide material forming the optical waveguide 30 should be provided by a printing method such as a screen printing or an offset printing. It is also preferable that the optical waveguide material forming the optical waveguide 30 should be provided by a slit coating method in which liquid resin is discharged from gaps of a slit shape. As the slit coating method, it is preferable that a desired medium such as resin should be applied to the substrate 10 by a capillary phenomenon.

[0085]

(Method of Fabricating Micro-tile Shaped Element)

Next, a method of fabricating micro-tile shaped elements forming the first micro-tile shaped element 21 and the second micro-tile shaped element 22 will be described with reference to Figs. 14 to 23. The micro-tile shaped elements of the present embodiment are fabricated by an epitaxial lift-off technique as its basis. In the present

fabricating method, although a case where a compound semiconductor device (a compound semiconductor element) of the micro-tile shaped element is attached to silicon/LSI chip forming a substrate is described, the present invention can be applied regardless of a kind of a semiconductor device or a kind of a LSI chip. In addition, although a 'semiconductor substrate' in the present embodiment represents an object made of a semiconductor material, the 'semiconductor substrate' is not limited to a plate-shaped substrate, but includes all kinds of shape, the shape being made of a semiconductor material.

[0086]

<First Step>

Fig. 14 is a schematic sectional view illustrating a first step of a method of fabricating the micro-tile shaped element. In Fig. 14, a substrate 110 is a semiconductor substrate, e.g., a GaAs compound semiconductor substrate. A sacrifice layer 111 is provided in the lowest layer of the substrate 110. The sacrifice layer 111 is made of aluminum-arsenic (AlAs), and its thickness is, for example, several hundred nm.

For example, a function layer 112 is provided on the upper side of the sacrifice layer 111. The thickness of the function layer 112 is, for example, in the range of 1 to 10(20) μm . Hence, a semiconductor device (a semiconductor

element) 113 is formed in the function layer 112. The semiconductor device 113 includes a light emitting diode (LED), a vertical cavity surface emitting laser (VCSEL), a photodiode (PD), a DFB laser, and the like. Such semiconductor device 113 is formed by stacking multi epitaxial layers on the substrate 110. Further, electrodes are formed in the semiconductor device 113 for operation test.

[0087]

<Second Step>

Fig. 15 is a schematic sectional view illustrating a second step of the method of fabricating the micro-tile shaped element. In the present step, partitioning grooves 121 are formed to partition each semiconductor device 113. The partitioning grooves 121 have the depth at least reachable to the sacrifice layer 111. For example, the width and the depth of the partitioning grooves are all in the range of ten to several hundred μm . Further, the partitioning grooves 121 are formed to be connected to each other such that the selective etching solution, which will be described later, can flow in the partitioning grooves 121. Moreover, it is desirable that the partitioning grooves 121 should be formed in a lattice shape like a go board.

Further, the distance between the partitioning grooves 121 is set to be in the range of several ten to several

hundred μm , so that each of the semiconductor device 113 partitioned and formed by the partitioning grooves 121 can have an area of the range of several ten to several hundred square μm . As a method of fabricating the partitioning grooves 121, a photolithography method and a wet etching or a dry etching method are used. In addition, it is preferable that the partitioning grooves 121 should be formed using U-shaped groove dicing within a range where crack is not generated in a substrate.

[0088]

<Third Step>

Fig. 16 is a schematic sectional view illustrating a third step of the method of fabricating the micro-tile shaped element. In the present step, an intermediate transfer film 131 is attached to the surface of the substrate 110 (semiconductor device 113 side). The intermediate transfer film 131 is a flexible band-shaped film with its surface coated with adhesion paste.

[0089]

<Fourth Step>

Fig. 17 is a schematic sectional view illustrating a fourth step of the method of fabricating the micro-tile shaped element. In the present step, the selective etching solution 141 is injected into partitioning grooves 121. In the present step, in order to selectively etch only the

sacrifice layer 111, the hydrochloric acid of low density, which has high selectivity to aluminum-arsenic, is used as the selective etching solution 141.

[0090]

<Fifth Step>

Fig. 18 is a schematic sectional view illustrating a fifth step of the method of fabricating the micro-tile shaped element. In the present step, after injecting the selective etching solution 141 into the partitioning grooves 121 in the fourth step, and then, the whole sacrifice layer 111 is selectively etched and removed from the substrate 110 with the passage of a predetermined time.

[0091]

<Sixth Step>

Fig. 19 is a schematic sectional view illustrating a sixth step of the method of fabricating the micro-tile shaped element. The entire sacrifice layer 111 is etched in the fifth step, and then the function layer 112 is detached from the substrate 110. Then, in the present step, the function layer 112, to which the intermediate transfer film 131 is attached, is detached from the substrate 110 by detaching the intermediate transfer film 131 from the substrate 110.

As a result, by forming the partitioning grooves 121 and etching the sacrifice layer 111, the function layer 112,

in which semiconductor devices 113 are formed, is segmented to be a semiconductor device ('micro-tile shaped element' of the aforementioned embodiment) having a predetermined shape (e.g., micro-tile shape) and to adhere to the intermediate transfer film 131. Herein, it is preferable that the function layer should have a thickness of, for example, 1 to 8 μm and a size (height and width) of, for example, several ten to several hundred μm .

[0092]

<Seventh Step>

Fig. 20 is a schematic sectional view illustrating a seventh step of the method of fabricating the micro-tile shaped element. In the present step, the intermediate transfer film 131 (to which micro-tile shaped elements 161 are attached) moves to align the micro-tile shaped elements 161 at a desired region of a final substrate 171. Herein, the final substrate 171 is composed of, for example, a silicon semiconductor or a integrated circuit chip (the integrated circuit chip 10d shown in Fig. 1), and an LSI region 172 (corresponding to circuit blocks 240a, 240b and 240c) is formed therein. Further, an adhesive 173 for attaching the micro-tile shaped elements 161 is previously applied to a desired region on the final substrate 171.

[0093]

<Eighth Step>

Fig. 21 is a schematic sectional view illustrating an eighth step of the method of fabricating the micro-tile shaped element. In the present step, micro-tile shaped elements 161, which are aligned to the desired portion of the final substrate 171, are pressed by a pin 181 with the intermediate transfer film 131, thereby adhering to the final substrate 171. Herein, because the adhesive 173 is applied to the desired portion, the micro-tile shaped elements 161 are attached to the desired portion of the final substrate 171.

[0094]

<Ninth Step>

Fig. 22 is a schematic sectional view illustrating a ninth step of the method of fabricating the micro-tile shaped element. In the present step, the micro-tile shaped elements 161 are detached from the intermediate transfer film 131 by vanishing the adhesion of the intermediate transfer film 131.

The adhesion of an adhesive for the intermediate transfer film 131 is vanished by UV light or heat. When using an adhesive having UV curable characteristics, the pin 181 is made of a transparent material, and UV light (UV) radiates from the front end of the pin 181, so the adhesion of the intermediate transfer film 131 is vanished. When using an adhesive having thermosetting characteristics, it

is preferable to heat the pin 181. Alternatively, after the sixth step, it is preferable that UV light should radiate to the entire surface of the intermediate transfer film 131, so the adhesion should be vanished. Although the adhesion is vanished, adhesiveness is substantially remained a little, and the micro-tile shaped elements 161 are very thin and light. Thus, the micro-tile shaped elements 161 are attached to the intermediate transfer film 131.

[0095]

<Tenth Step>

The present step is not shown. In the present step, the micro-tile shaped elements 161 are attached to the final substrate 171 by a thermal processing.

[0096]

<Eleventh Step>

Fig. 23 is a schematic sectional view illustrating an eleventh step of the method of fabricating the micro-tile shaped element. In the present step, an electrode of the micro-tile shaped element 161 is electrically connected through wiring 191 to circuits on the final substrate 171 to form one LSI chip (an integrated circuit chip for an optical interconnection circuit). It is preferable that a quartz substrate or a plastic film as well as a silicon semiconductor should be used as the final substrate 171.

[0097]

(Example of Application)

Hereinafter, an example of the application of the wavelength multiplexing on-chip optical interconnection circuit according to the present invention will be described.

For example, the wavelength multiplexing on-chip optical interconnection circuits of the above-described embodiments are used as signal transmission means of an optoelectronics integrated circuit system. A computer is used as the optoelectronics integrated circuit system. Next, circuit blocks constituting a CPU are formed on the integrated circuit chip 10d as shown in Fig. 1, and circuit blocks constituting a storage device are also formed on the integrated circuit chip 10d. Although signal processing in the circuit blocks constituting the CPU or the storage device is performed using electrical signals, the wavelength multiplexing on-chip optical interconnection circuit of the above described embodiment is applied to the data transmission between such circuit blocks.

[0098]

As a result, in accordance with the present application, it is possible to greatly improve the signal transmission speed of bus, which has become bottleneck in a computer processing speed, as compared with the prior art while being simple and easy configuration. Further, according to the

present application, it is possible to make miniaturize and to high-perform the computer system in a large range.

[0099]

(Electronic Apparatus)

An electronic apparatus having a wavelength multiplexing on-chip optical interconnection circuit or a flat panel display of the above-described embodiments will be described.

Fig. 24 is a perspective view illustrating an example of a cellular phone. In Fig. 24, reference numeral 1000 represents a body of a cellular phone using the above-described wavelength multiplexing on-chip optical interconnection circuit, and reference numeral 1001 represents a display part using the above-described flat panel display (an electro-optical device).

[0100]

Fig. 25 is a perspective view illustrating an example of a wristwatch type electro-optical apparatus. In Fig. 25, reference numeral 1100 represents a body of a watch using the above-described wavelength multiplexing on-chip optical interconnection circuit, and reference numeral 1101 represents a display part using the above-described flat panel display (an electro-optical device)..

[0101]

Fig. 26 is a perspective view illustrating an example

of a portable information processing device such as a word-processor or PC. In Fig. 26, reference numeral 1200 represents an information processing device, reference numeral 1202 represents an input part such as a keyboard, reference numeral 1204 represents a body of the information processing device using the above-described wavelength multiplexing on-chip optical interconnection circuit, and reference numeral 1206 represents a display part using the above-described flat panel display (an electro-optical device).

[0102]

Since the electronic apparatus shown in Figs. 24 to 26 have the above-described wavelength multiplexing on-chip optical interconnection circuit or the above-described flat panel display, it is possible to achieve an electronic apparatus having a display part with a high display quality, high response speed, and the bright and large screen. Further, as compared with a conventional art, it is possible to provide a high-performance electronic apparatus in which information processing speed is fast by using the above-described wavelength multiplexing on-chip optical interconnection circuit. Moreover, it is possible to miniaturize electronic apparatus, and to reduce manufacturing costs by using the above-described wavelength multiplexing on-chip optical interconnection circuit.

[0103]

Further, the technical scope of the present invention is not limited to the above-described embodiments, and various modifications can be made without departing from the spirit or scope of the present invention. It is also intended that specific materials or configurations illustrated in the embodiments are only examples and suitable modifications can be made.

[Brief Description of the Drawings]

[Fig. 1]

Fig. 1 is a perspective view of an integrated circuit chip according to the first embodiment of the present invention.

[Fig. 2]

Fig. 2 is a side view and a plan view of a circuit element according to the embodiment of the present invention.

[Fig. 3]

Fig. 3 is a side view illustrating a modified example of the circuit element described above.

[Fig. 4]

Fig. 4 is a side view illustrating a modified example of the circuit element described above.

[Fig. 5]

Fig. 5 is a side view illustrating a modified example of the circuit element described above.

[Fig. 6]

Fig. 6 is a side view and a plan view illustrating a modified example of the circuit element described above.

[Fig. 7]

Fig. 7 is a side view and a plan view illustrating a modified example of the circuit element described above.

[Fig. 8]

Fig. 8 is a side view and a plan view illustrating a modified example of the circuit element described above.

[Fig. 9]

Fig. 9 is a side view and a plan view illustrating a modified example of the circuit element described above.

[Fig. 10]

Fig. 10 is typical side views illustrating a fabricating method according to an embodiment of the present invention.

[Fig. 11]

Fig. 11 is typical side views illustrating another fabricating method according to the embodiment of the present invention.

[Fig. 12]

Fig. 12 is typical side views illustrating another fabricating method according to the embodiment of the present invention.

[Fig. 13]

Fig. 13 is typical side views illustrating another fabricating method according to the embodiment of the present invention.

[Fig. 14]

Fig. 14 is a schematic sectional view illustrating a first step of a method of fabricating a micro-tile shaped element.

[Fig. 15]

Fig. 15 is a schematic sectional view illustrating a second step of the fabricating method described above.

[Fig. 16]

Fig. 16 is a schematic sectional view illustrating a third step of the fabricating method described above.

[Fig. 17]

Fig. 17 is a schematic sectional view illustrating a fourth step of the fabricating method described above.

[Fig. 18]

Fig. 18 is a schematic sectional view illustrating a fifth step of the fabricating method described above.

[Fig. 19]

Fig. 19 is a schematic sectional view illustrating a sixth step of the fabricating method described above.

[Fig. 20]

Fig. 20 is a schematic sectional view illustrating a seventh step of the fabricating method described above.

[Fig. 21]

Fig. 21 is a schematic sectional view illustrating an eighth step of the fabricating method described above.

[Fig. 22]

Fig. 22 is a schematic sectional view illustrating a ninth step of the fabricating method described above.

[Fig. 23]

Fig. 23 is a schematic sectional view illustrating an eleventh step of the fabricating method described above.

[Fig. 24]

Fig. 24 is a diagram illustrating an example of an electronic apparatus comprising the circuit according to the present embodiment.

[Fig. 25]

Fig. 25 is a diagram illustrating an example of an electronic apparatus comprising the circuit according to the present embodiment.

[Fig. 26]

Fig. 26 is a diagram illustrating an example of an electronic apparatus comprising the circuit according to the present embodiment.

[Reference Numerals]

10d: INTEGRATED CIRCUIT CHIP

21, 21': FIRST MICRO-TILE SHAPED ELEMENT

21a: LIGHT EMITTING PART

22, 22': SECOND MICRO-TILE SHAPED ELEMENT

22b: LIGHT RECEIVING PART

30: OPTICAL WAVEGUIDE

231: METAL WIRING LINE

240a, 240b, 240c: CIRCUIT BLOCK

[Name of Document] ABSTRACT

[Abstract]

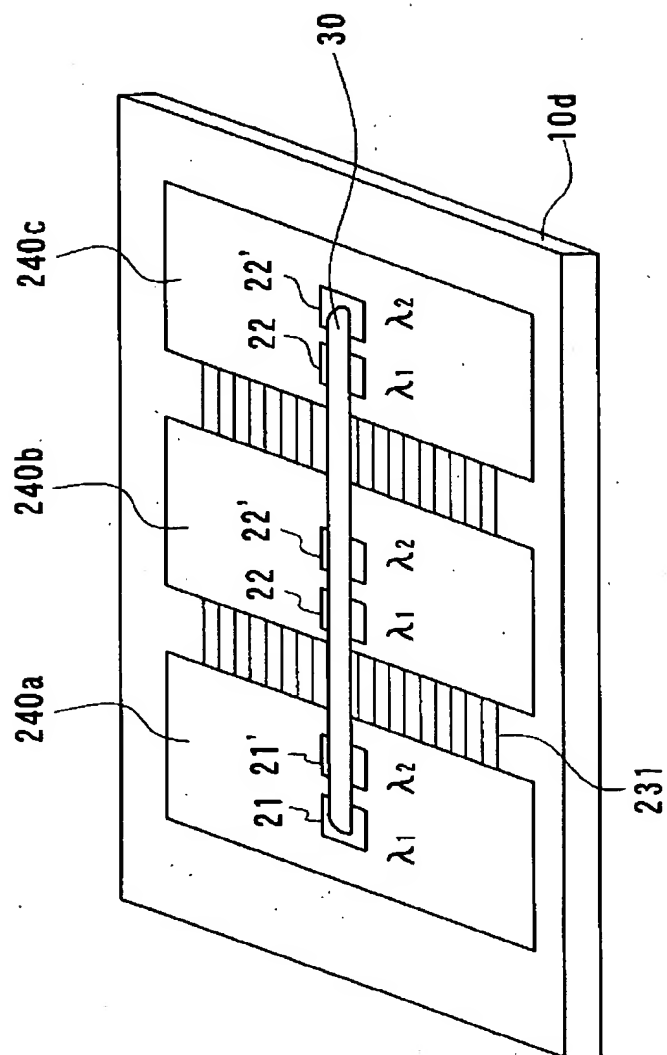
[Object] To provide a wavelength multiplexing on-chip optical interconnection circuit, an electro-optical device, and an electronic apparatus, wherein a signal transmission speed can be enhanced, fineness can be easily accomplished, and they can be simply manufactured.

[Solving Means] The wavelength multiplexing on-chip optical interconnection circuit comprises a plurality of circuit blocks 240a, 240b, and 240c provided on one integrated circuit chip 10d; and an optical waveguide 30 provided on the integrated circuit chip 10d, as a transmission line for transmitting a plurality of light components having different wavelengths (λ_1 , λ_2) between the circuit blocks 240a, 240b, and 240c.

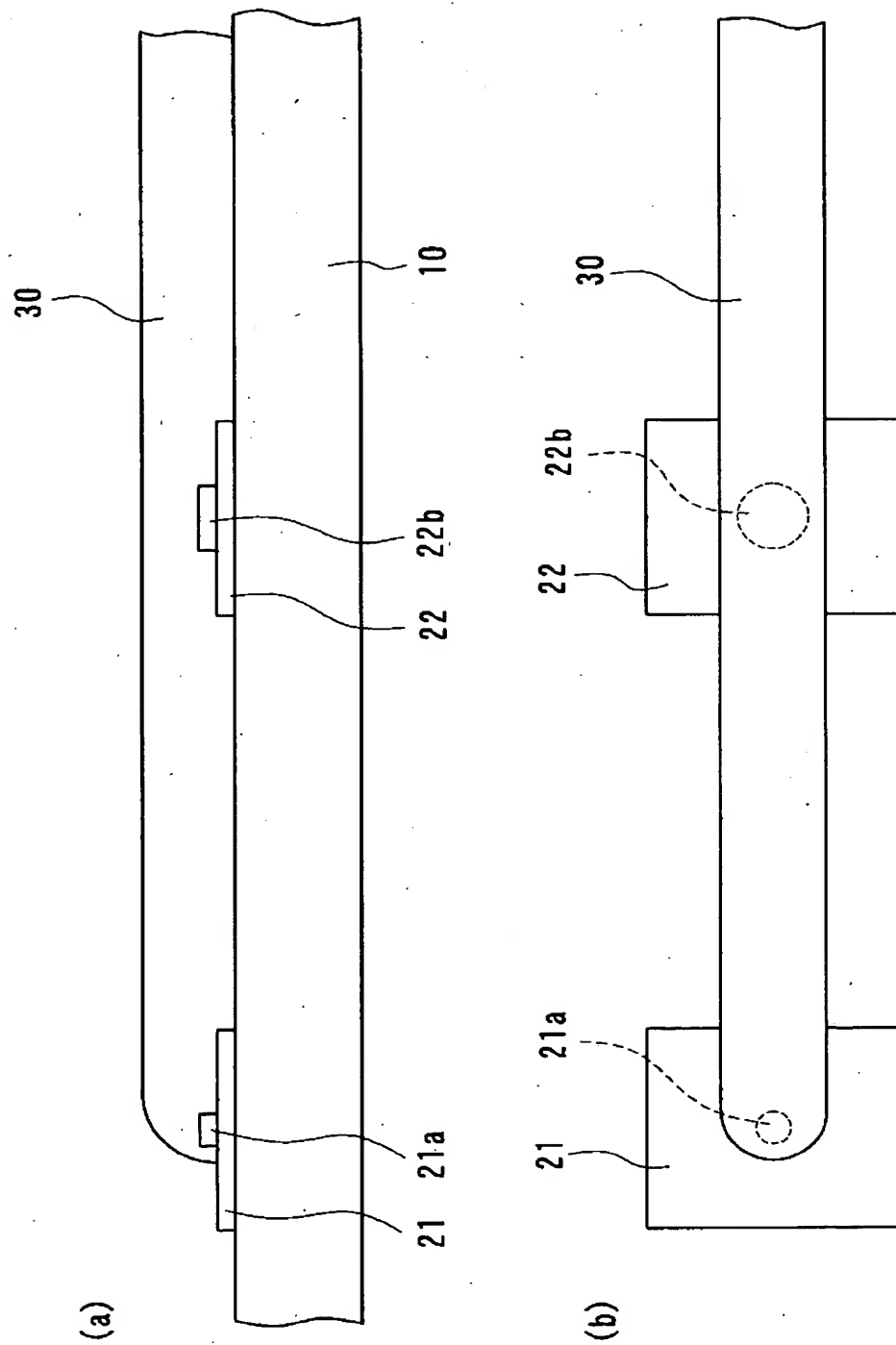
[Selected Figure] Fig. 1

[Name of Document] DRAWINGS

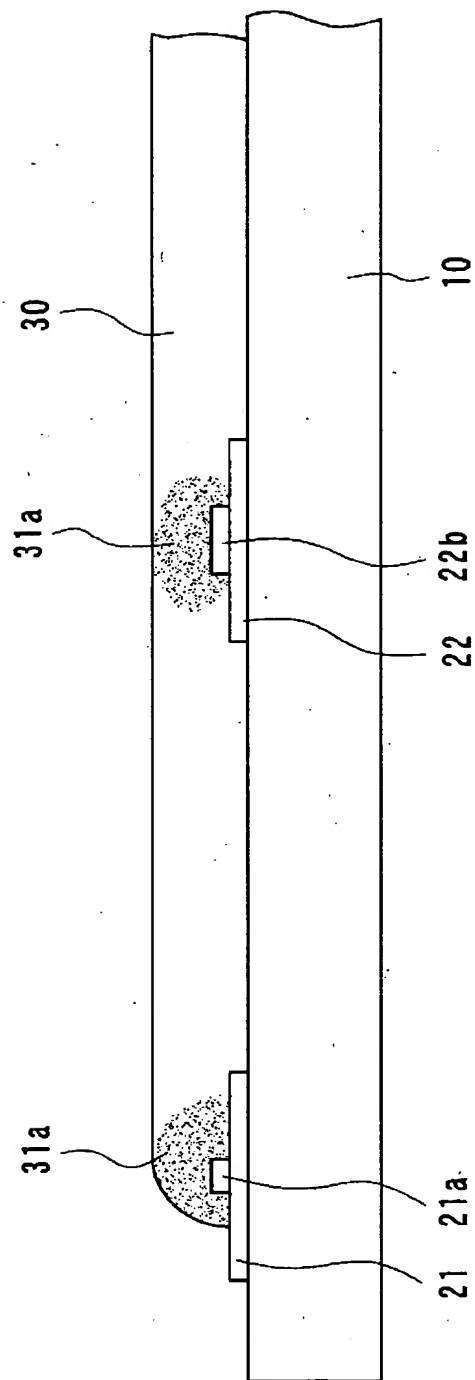
[FIG. 1]



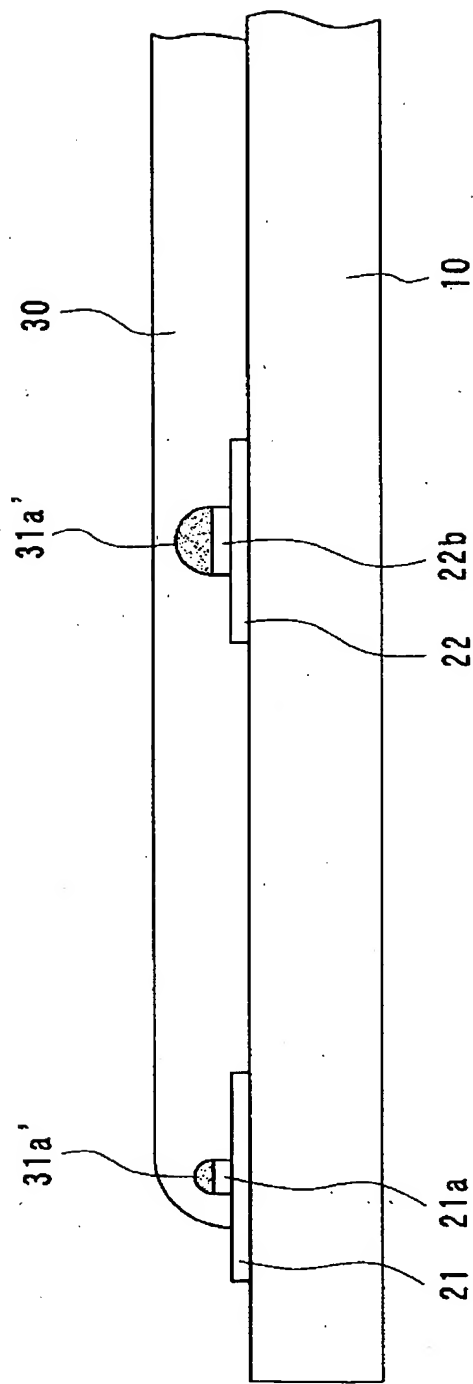
[FIG. 2]



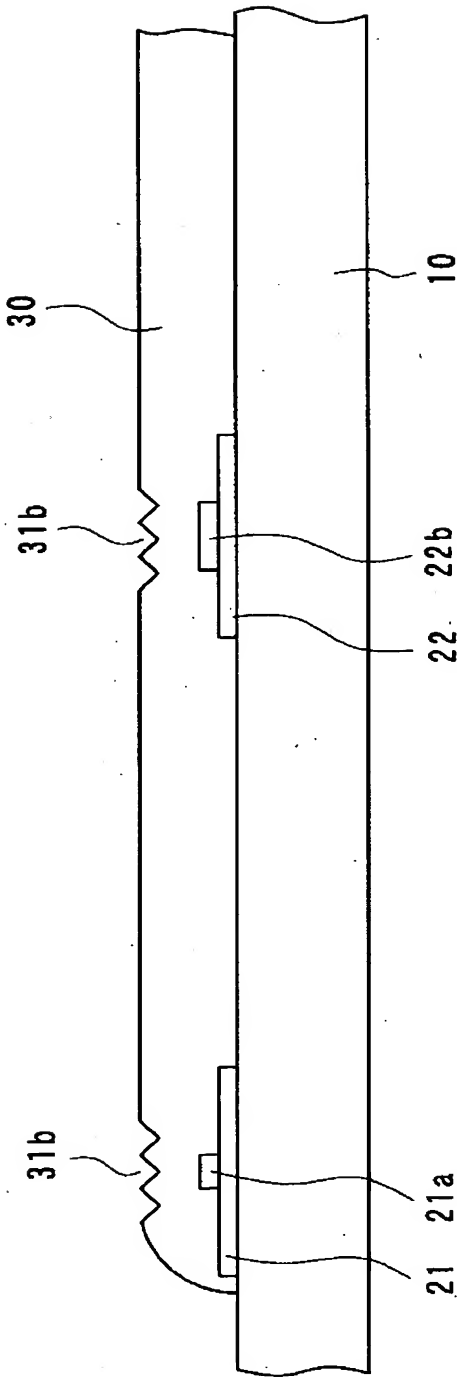
[FIG. 3]



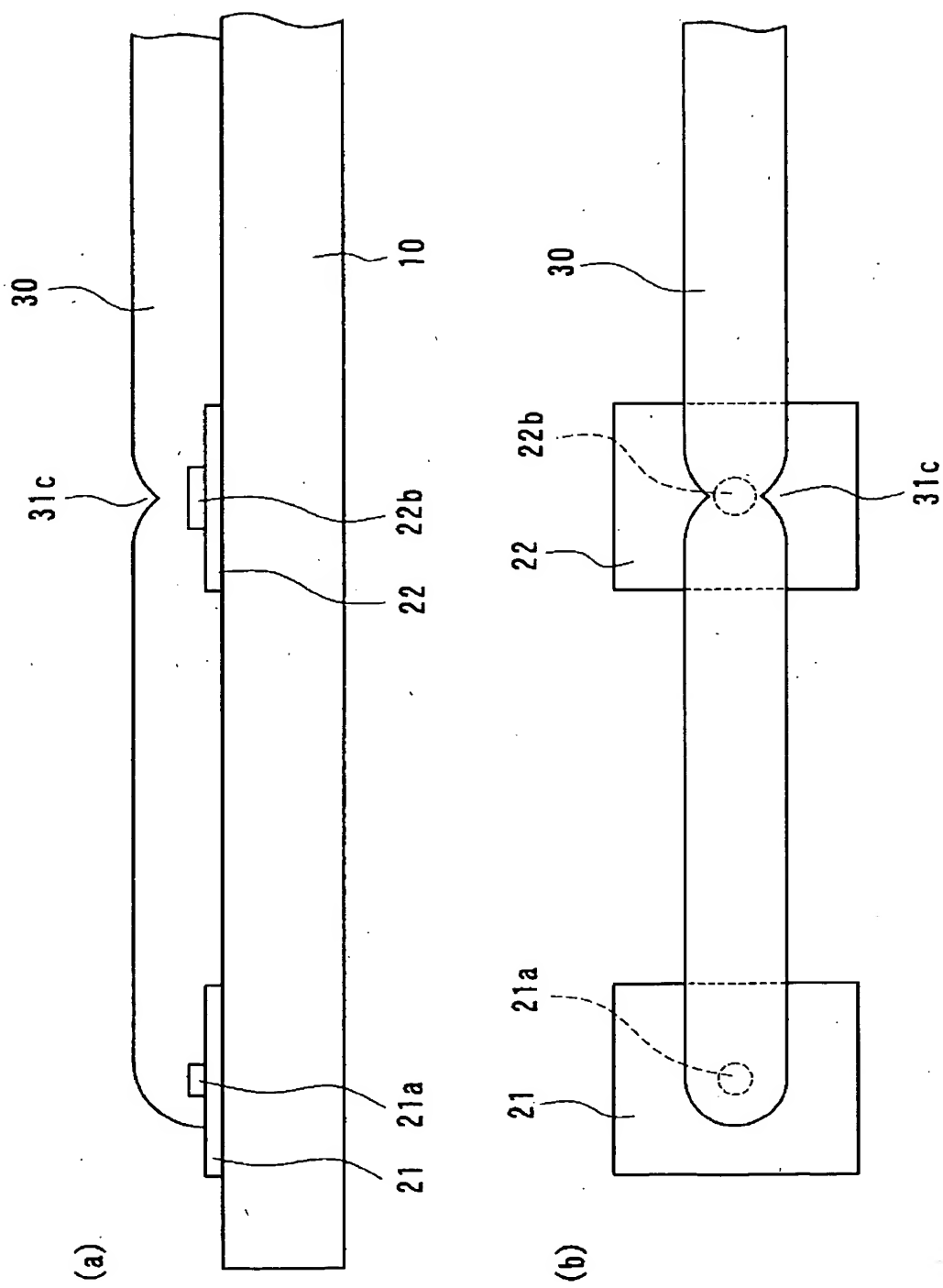
[FIG. 4]



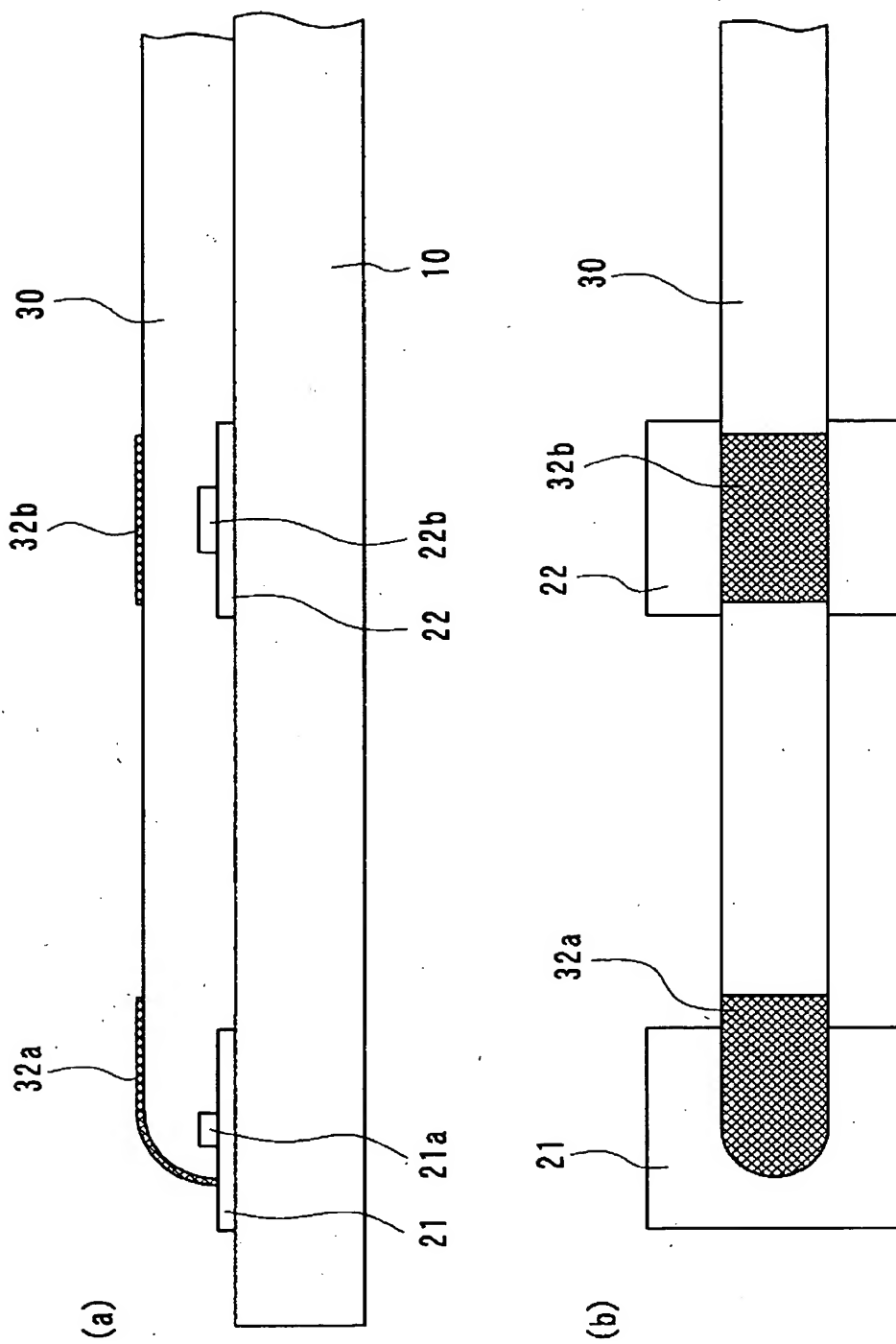
[FIG. 5]



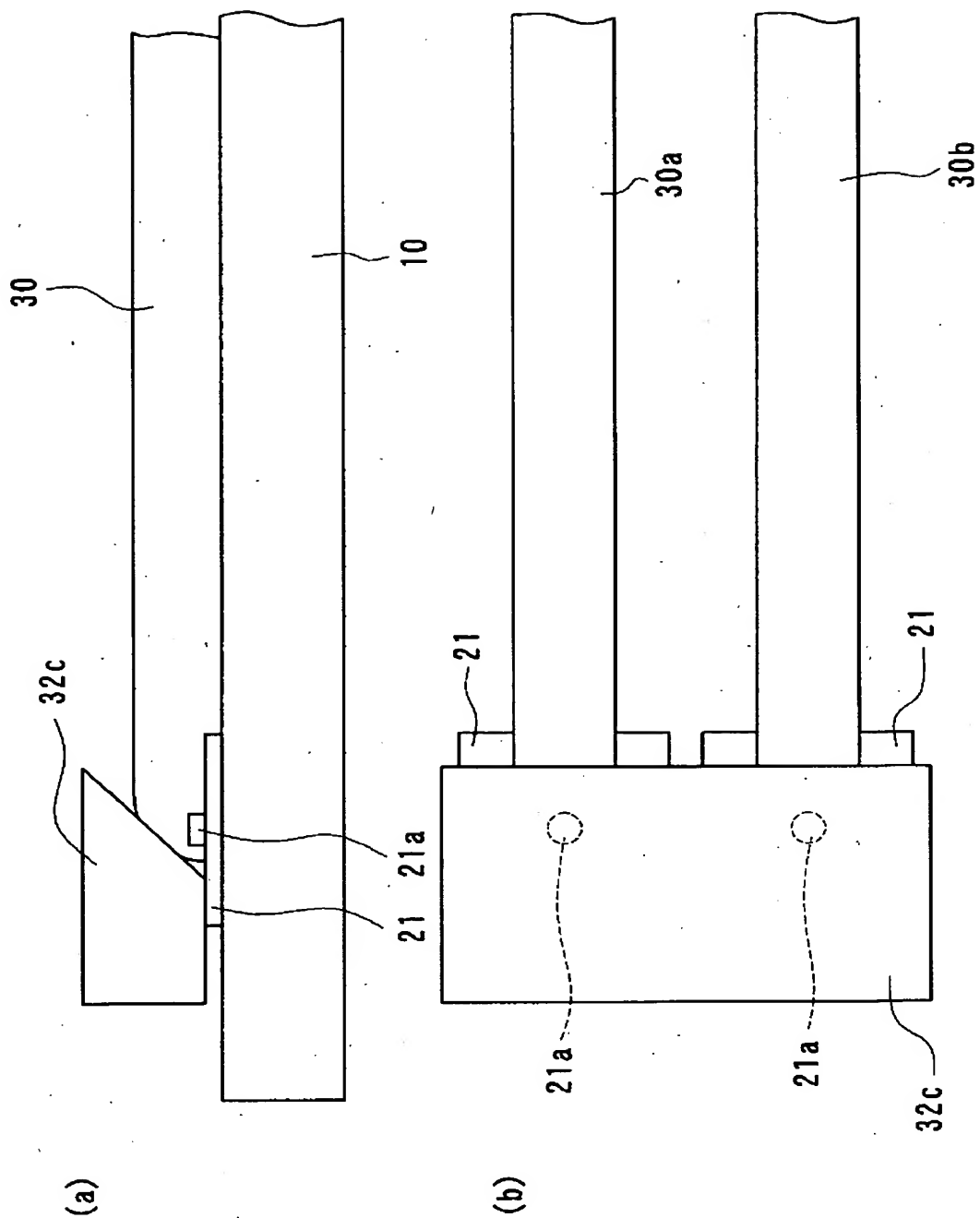
[FIG. 6]



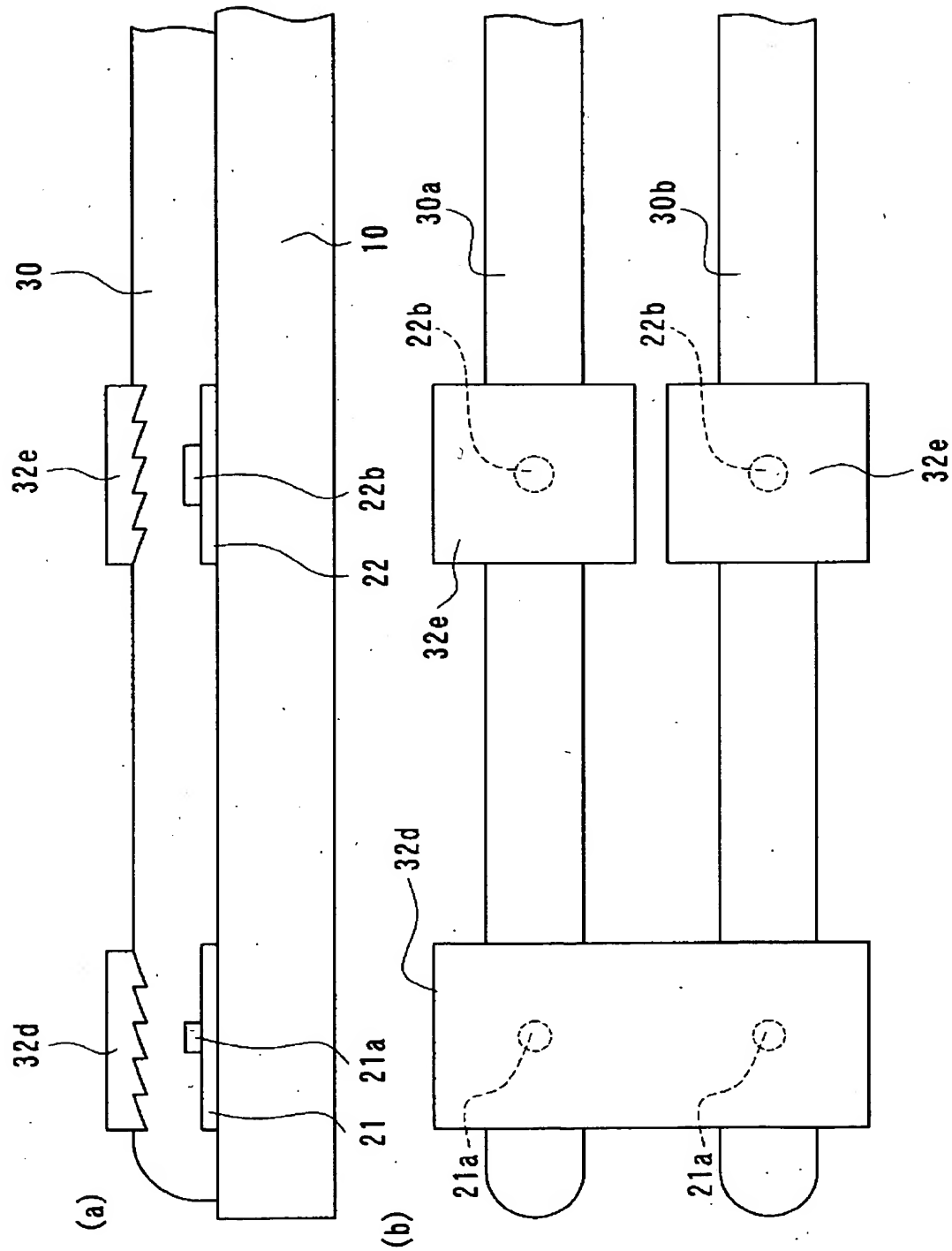
[FIG. 7]



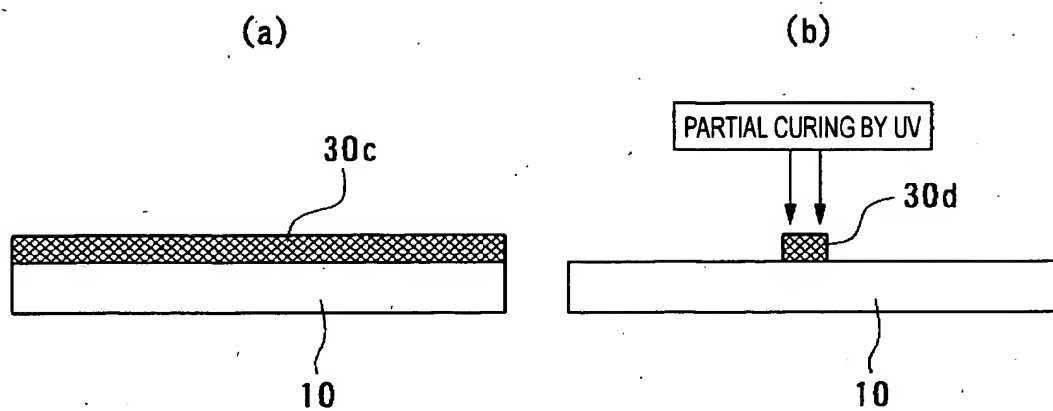
[FIG. 8]



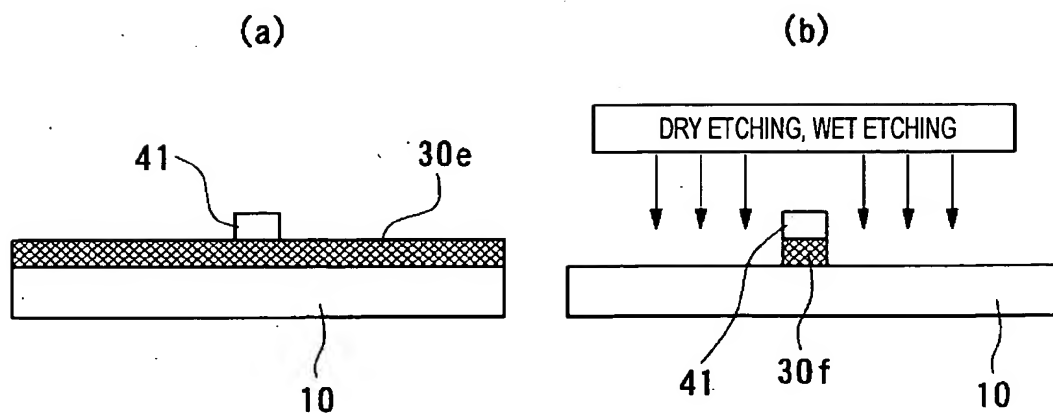
[FIG. 9]



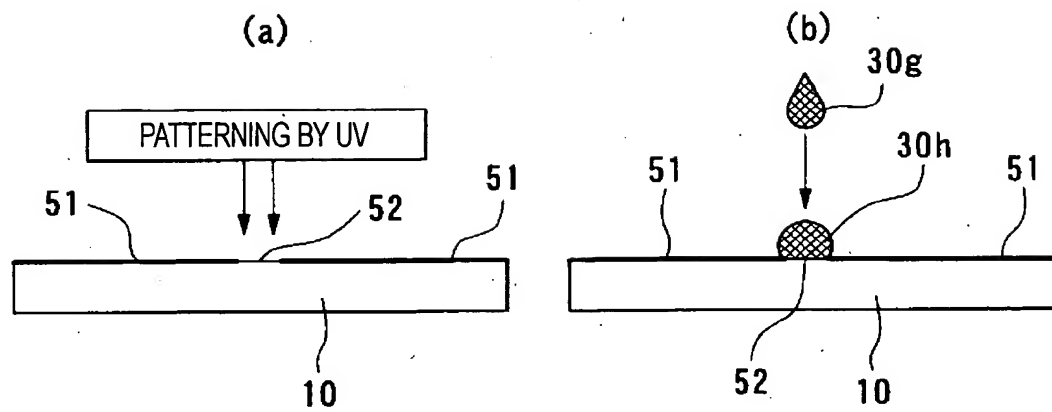
[FIG. 10]



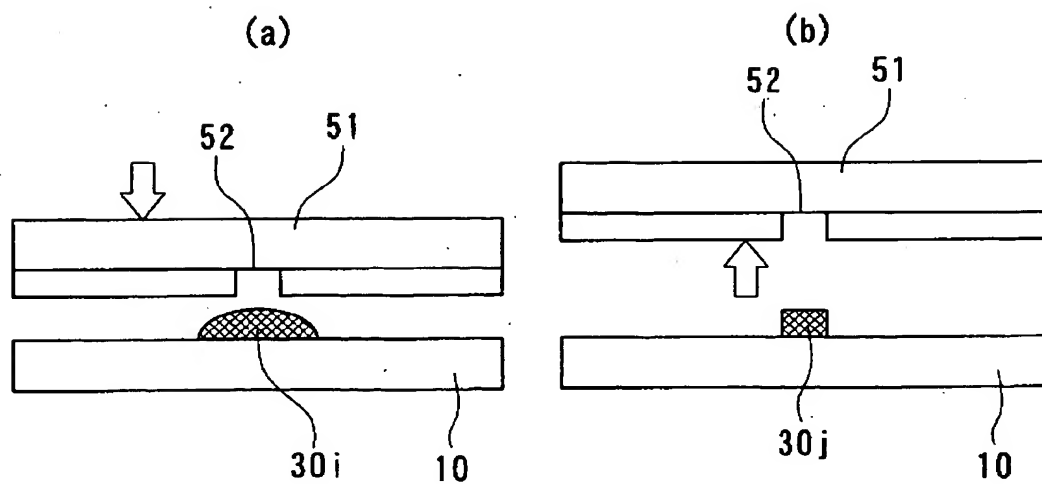
[FIG. 11]



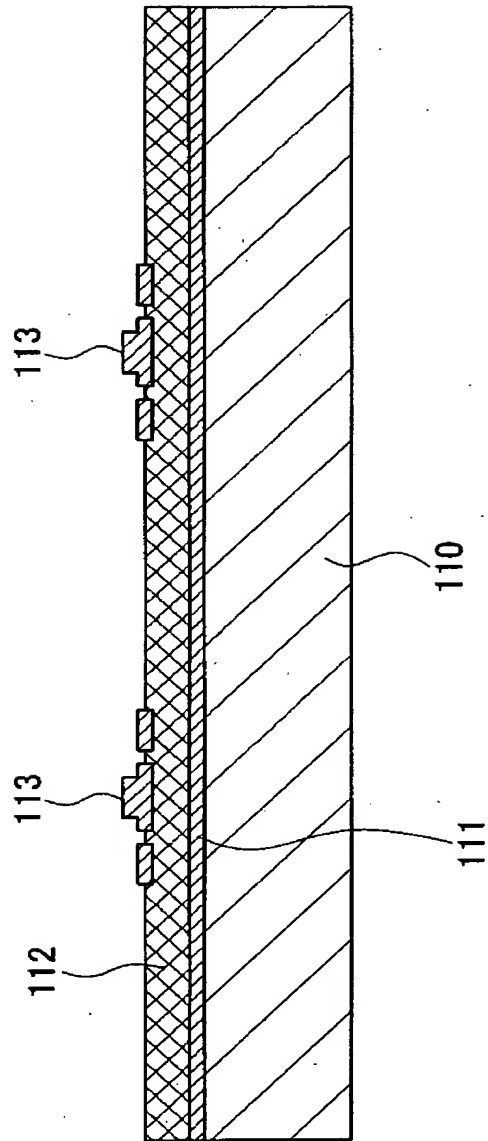
[FIG. 12]



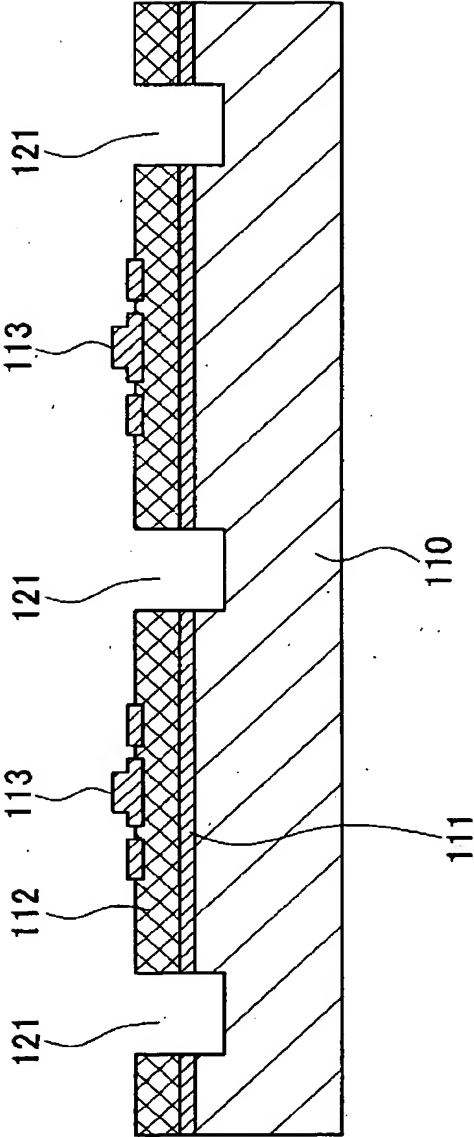
[FIG. 13]



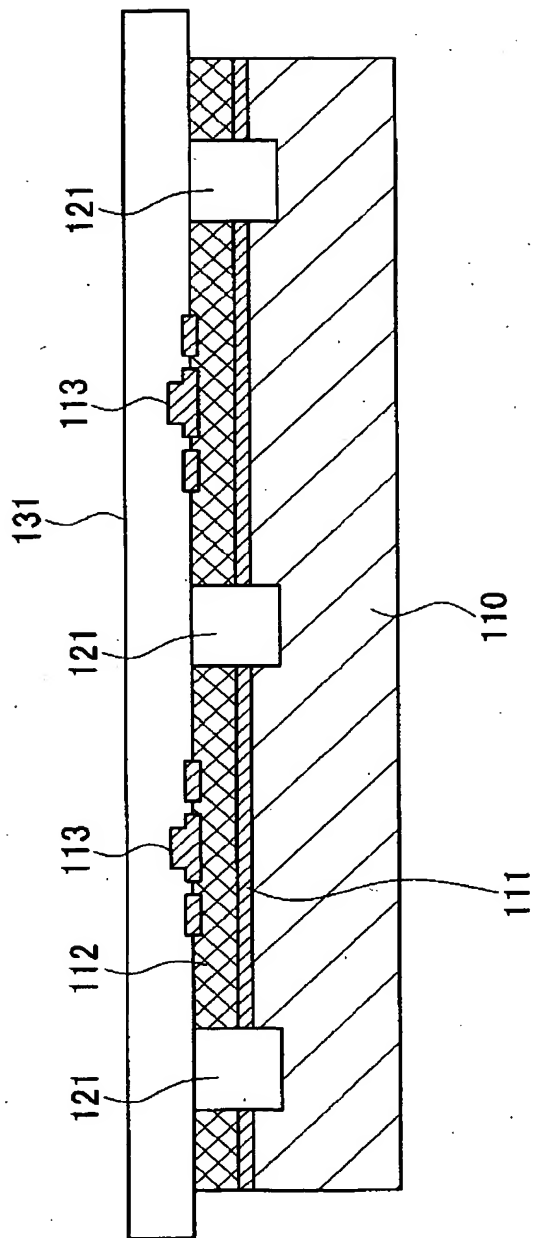
[FIG. 14]



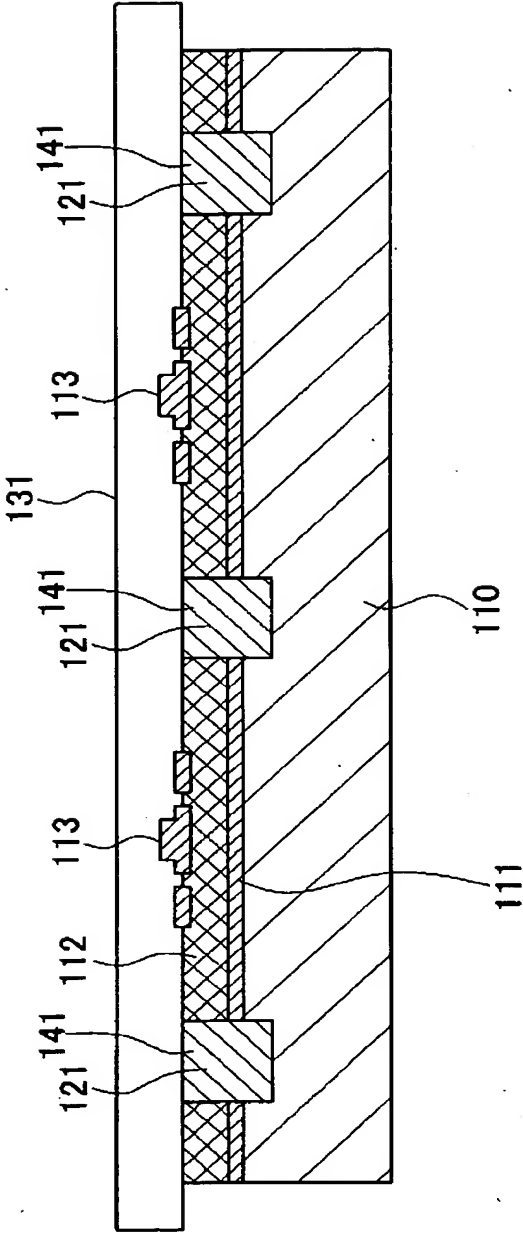
[FIG. 15]



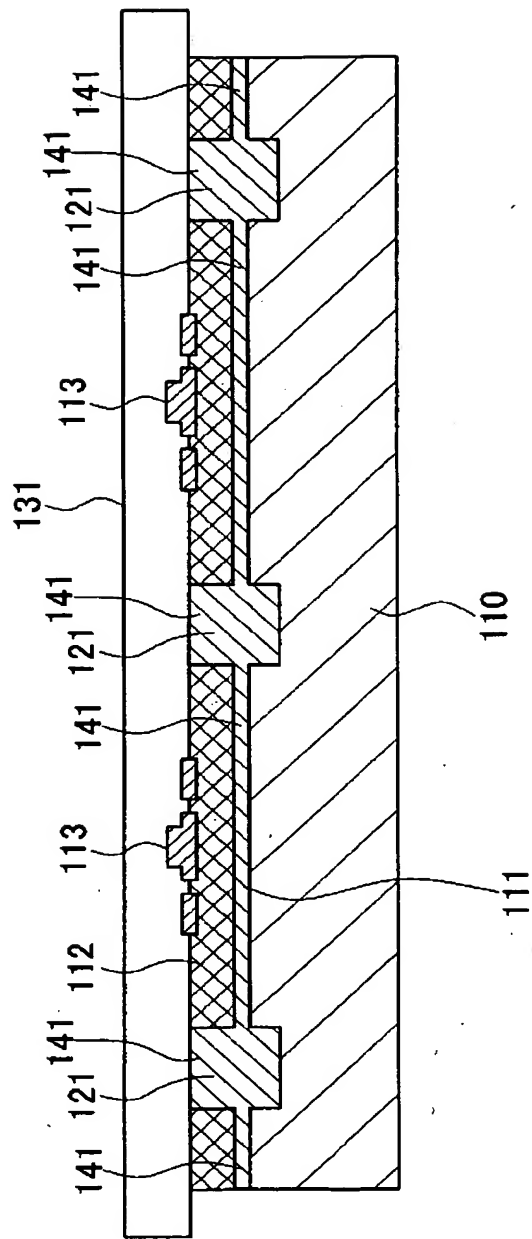
[FIG. 16]



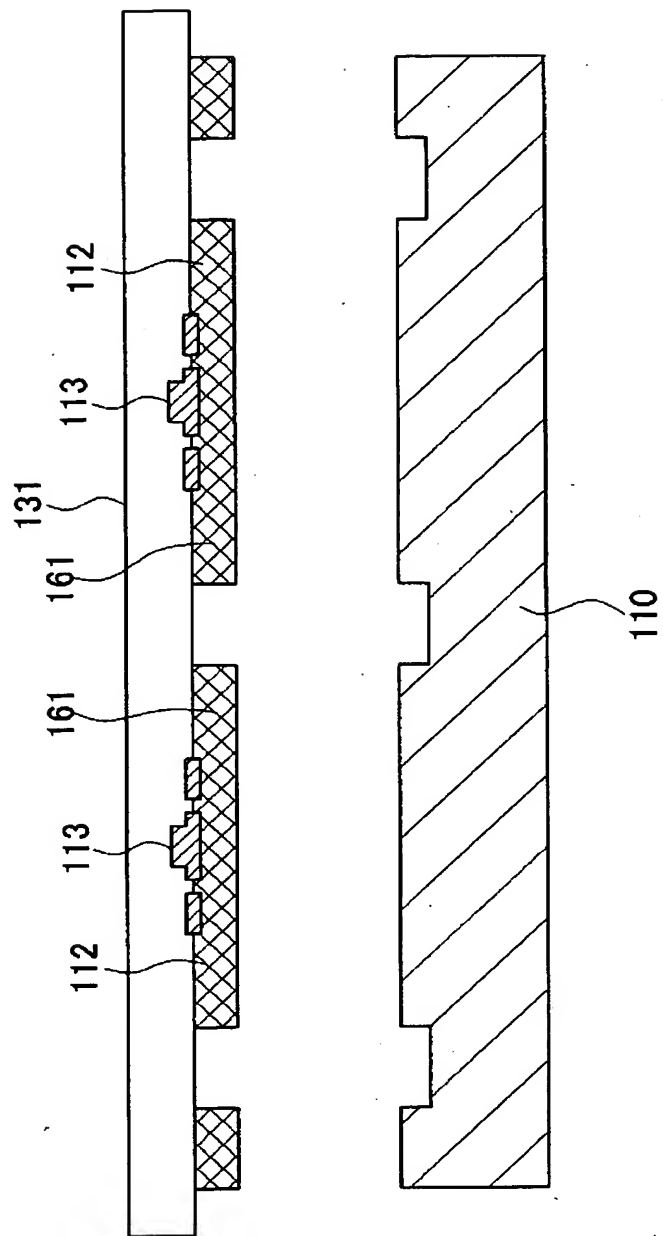
[FIG. 17]



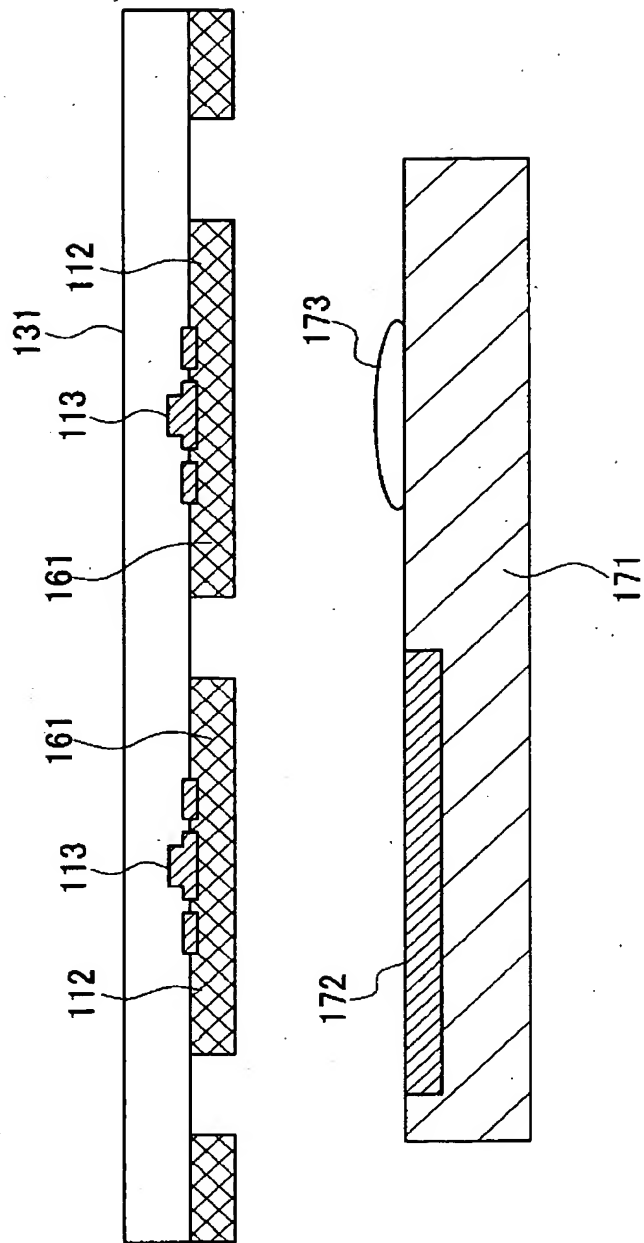
[FIG. 18]



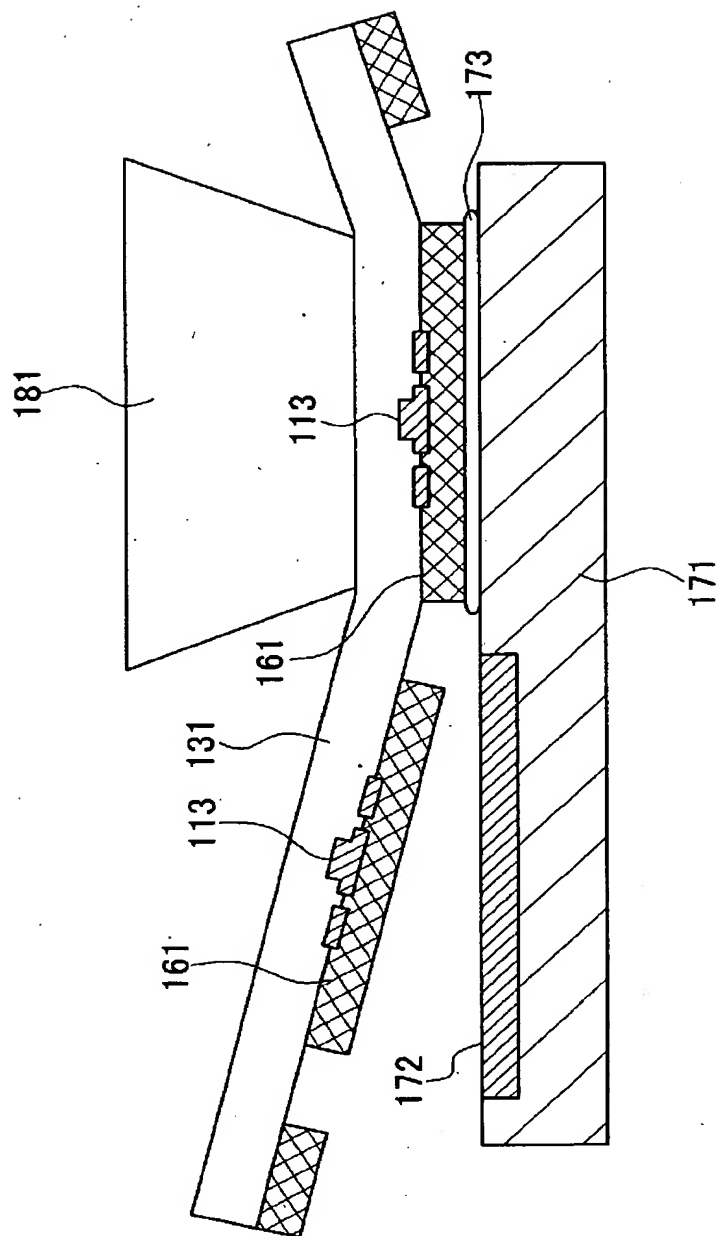
[FIG. 19]



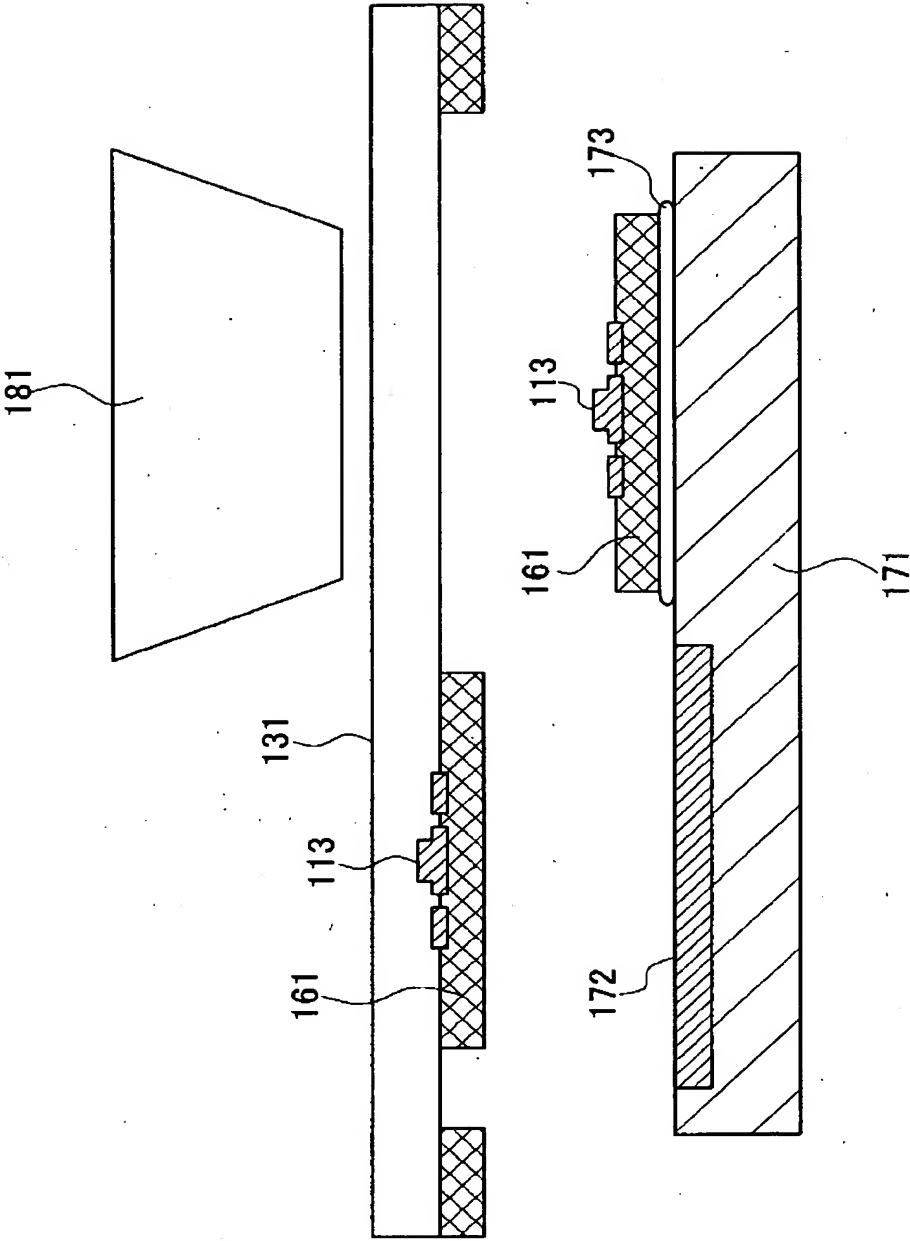
[FIG. 20]



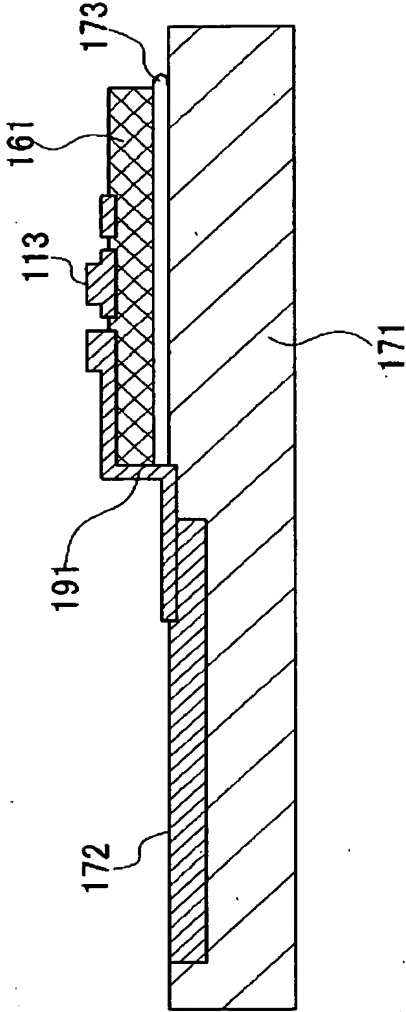
3



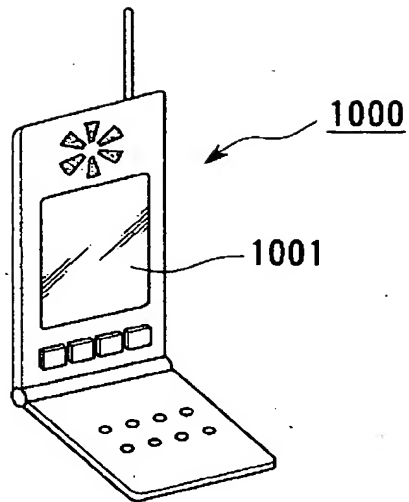
[FIG. 22]



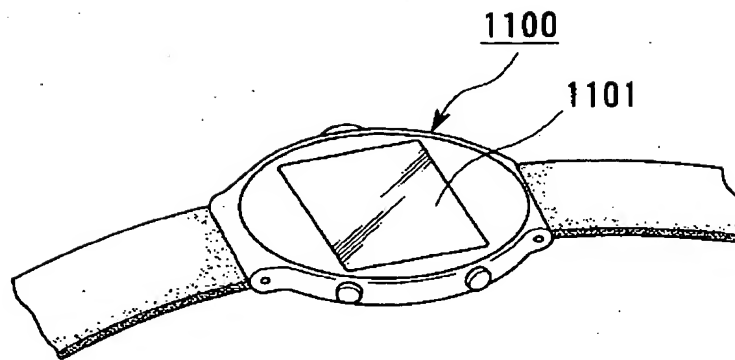
[FIG. 23]



[FIG. 24]



[FIG. 25]



[FIG. 26]

